

FIG. 1

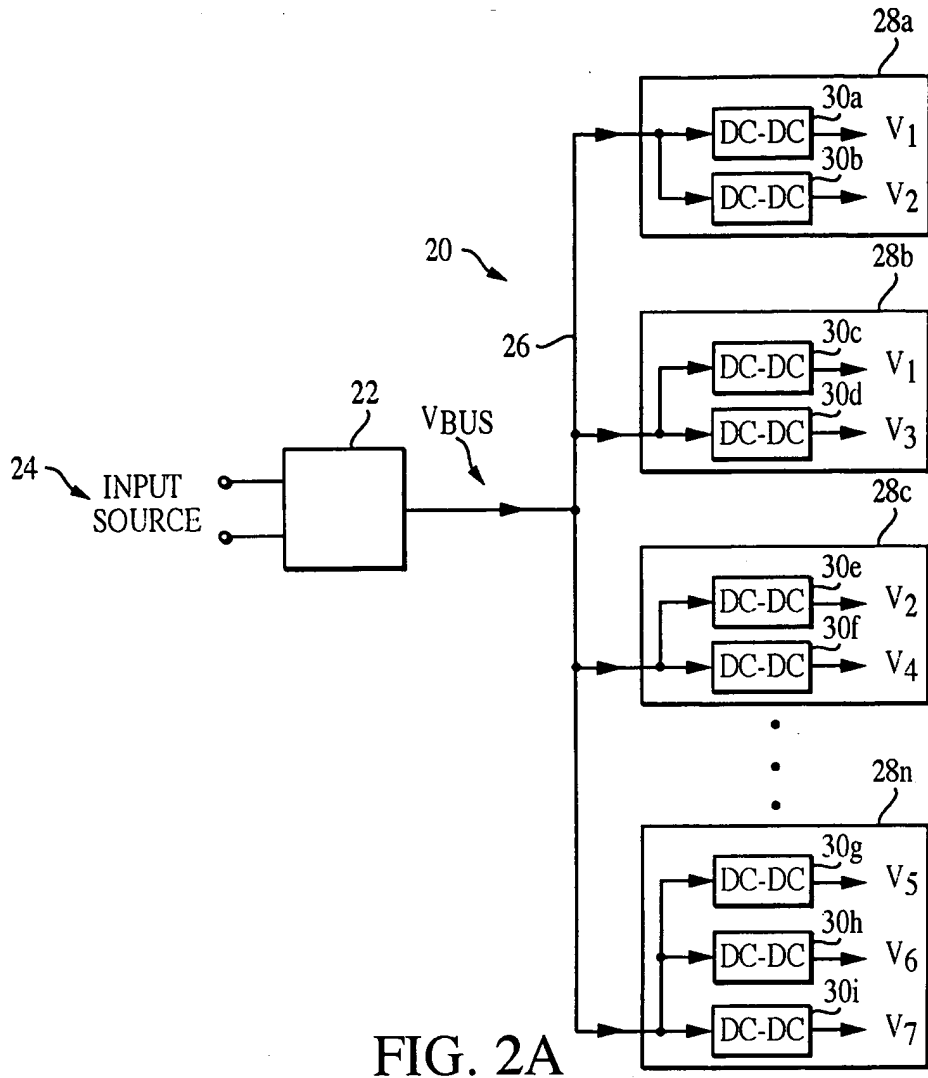


FIG. 2A

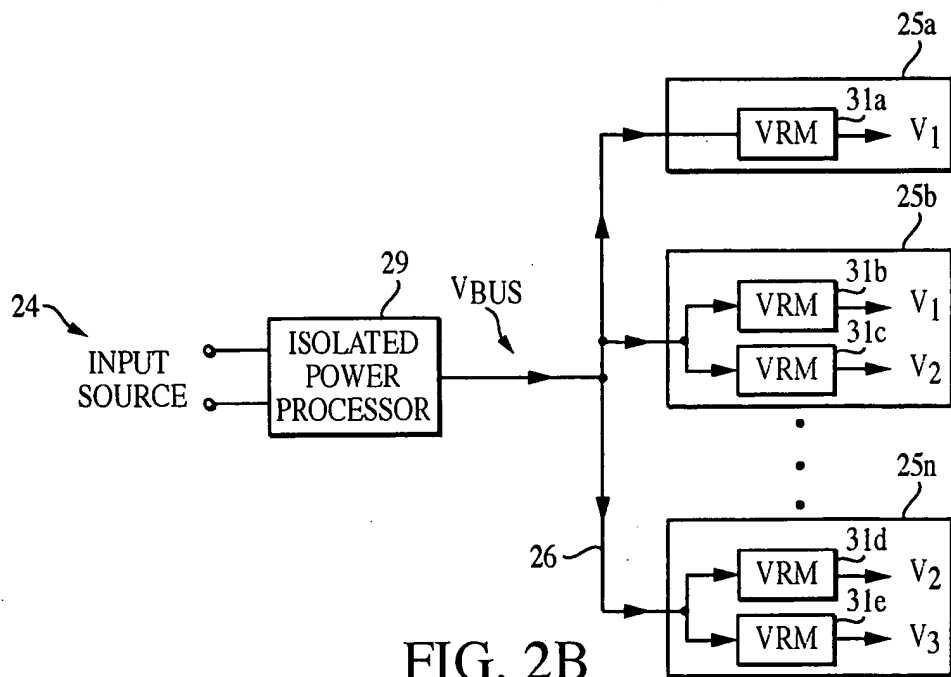


FIG. 2B

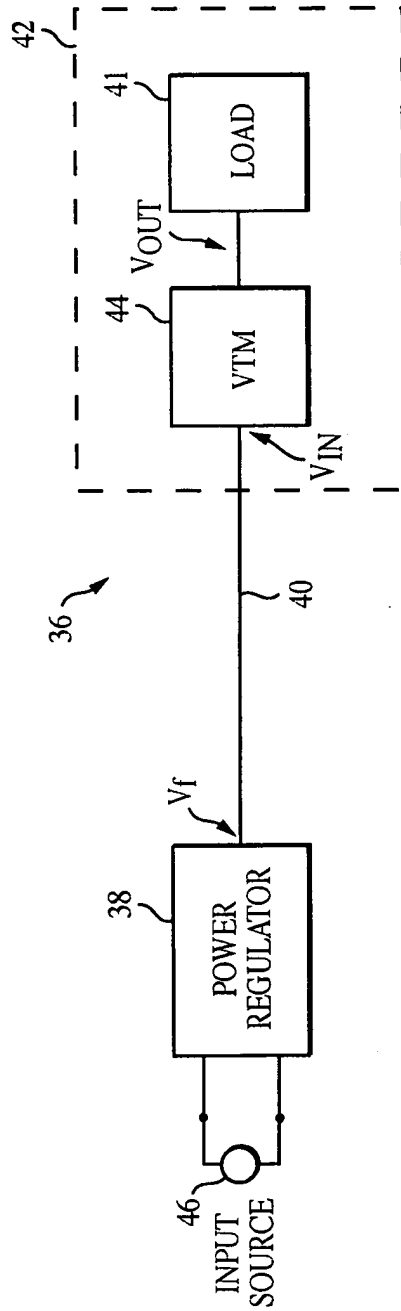


FIG. 3A

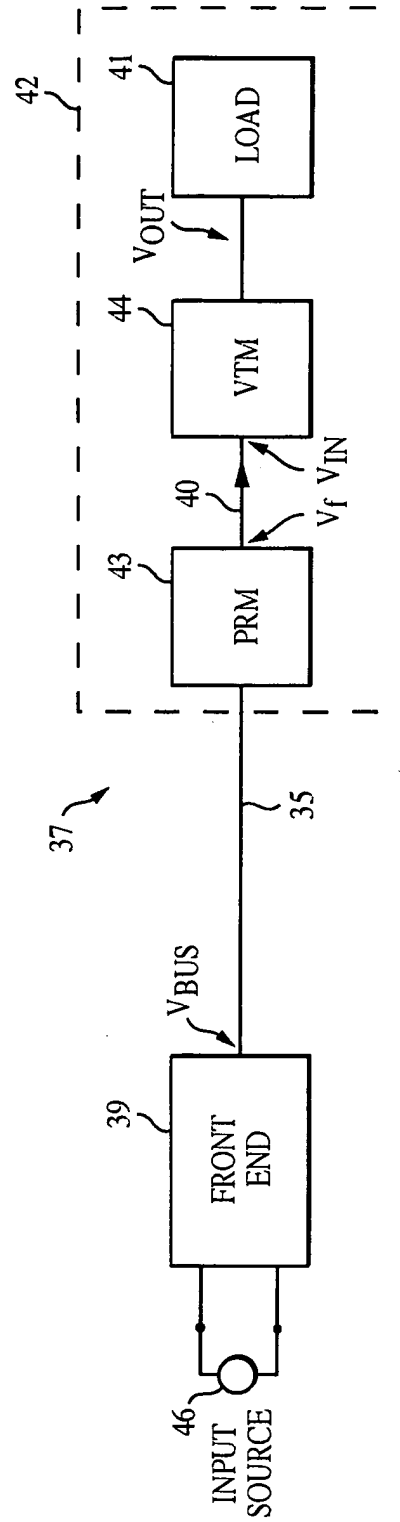


FIG. 3B

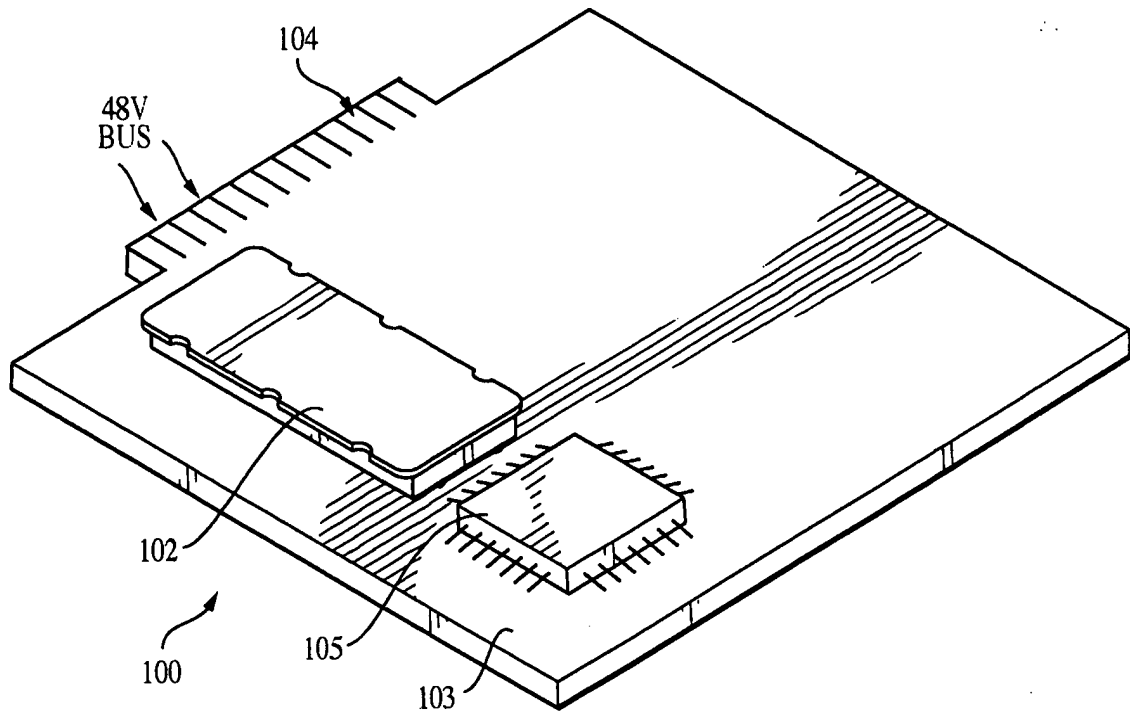


FIG. 4A

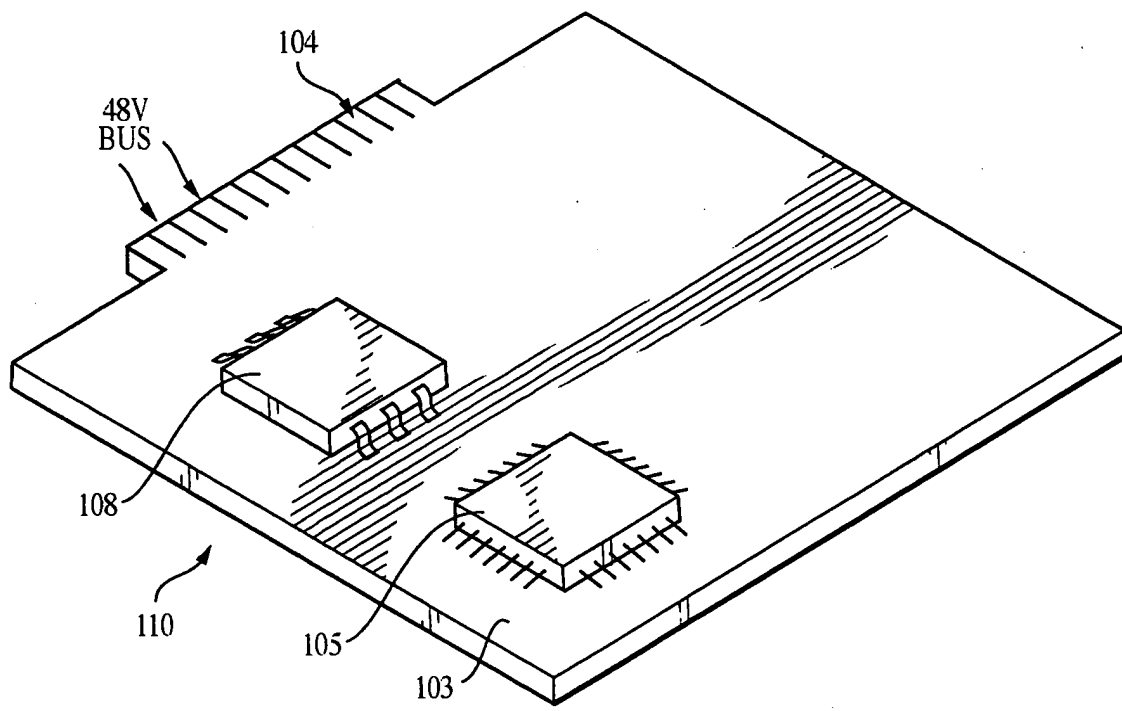


FIG. 4B

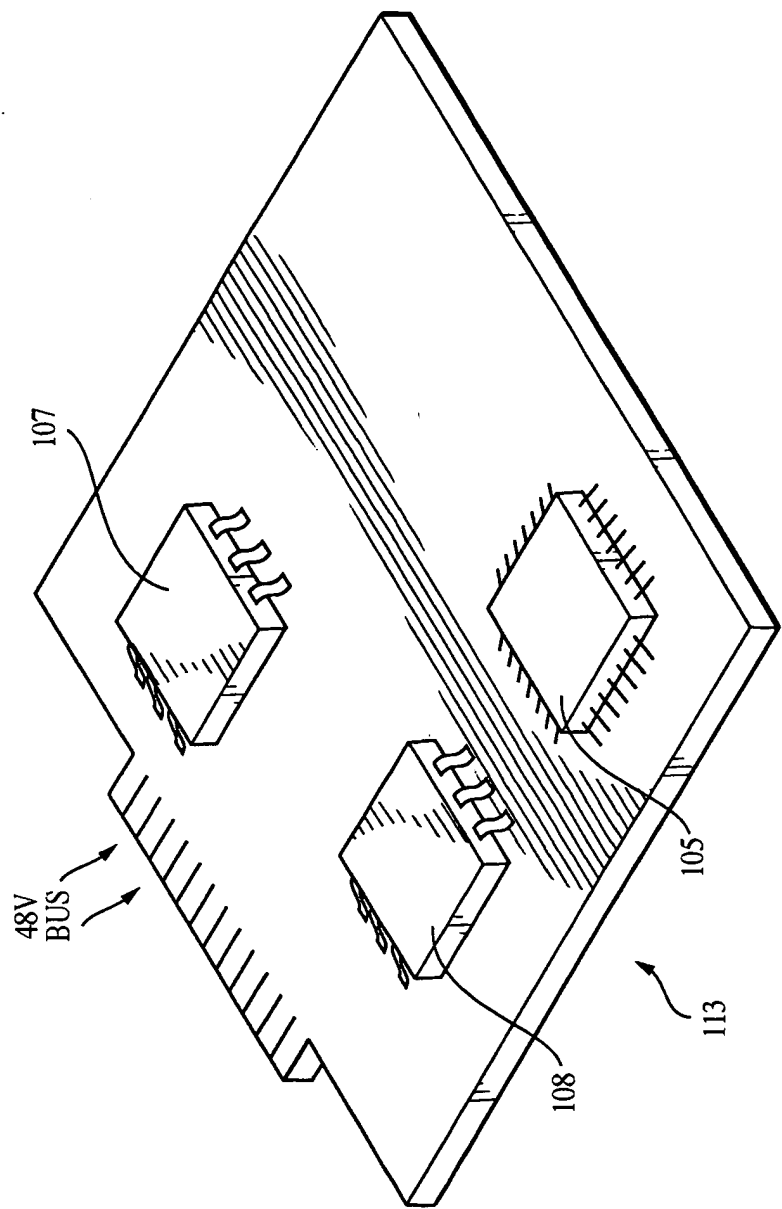


FIG. 4C

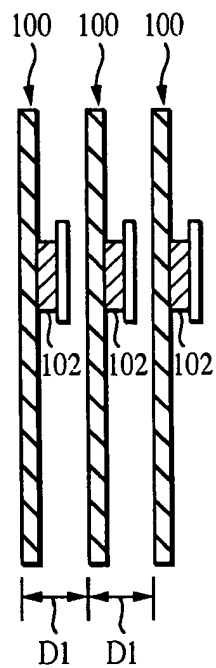


FIG. 5A

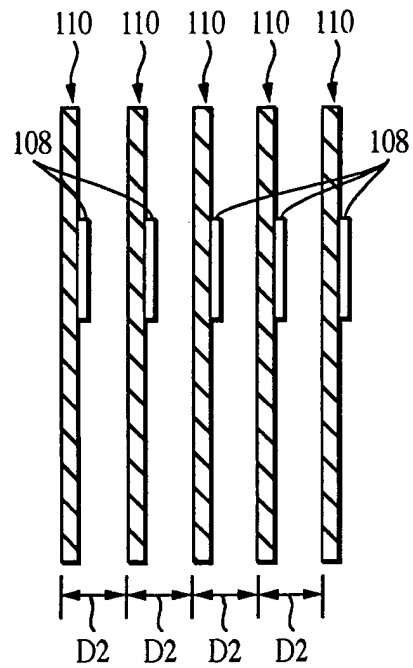


FIG. 5B

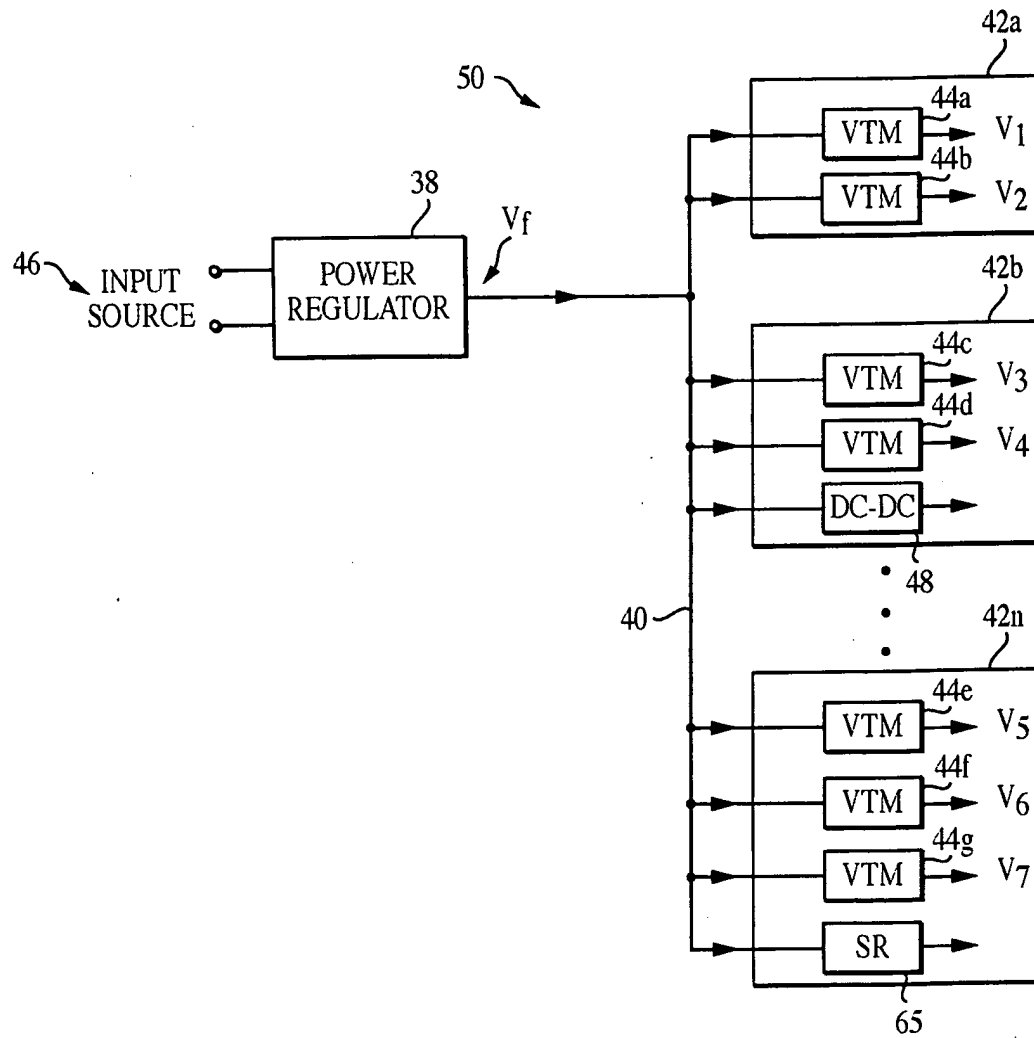


FIG. 6

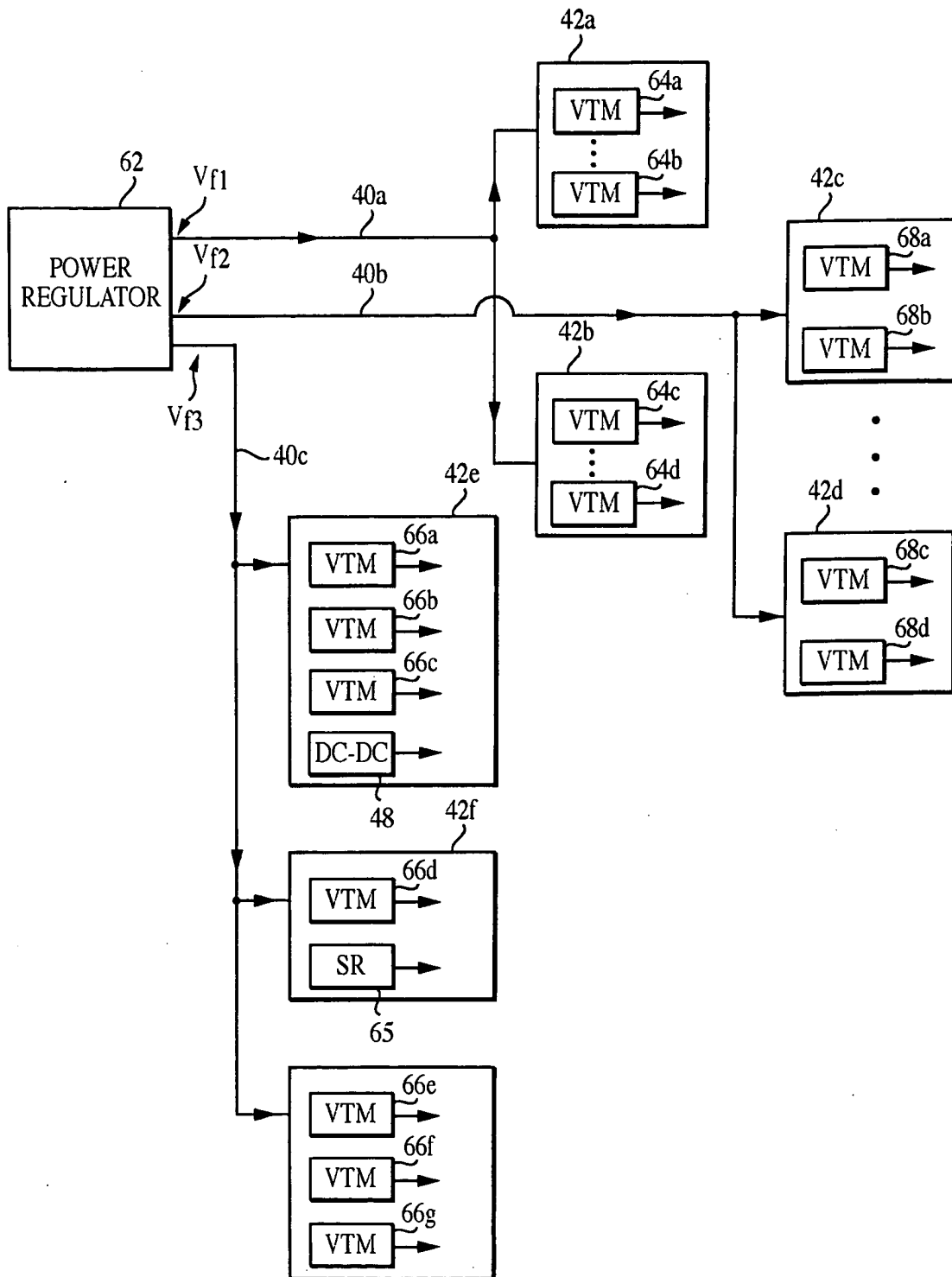


FIG. 7



FIG. 8A

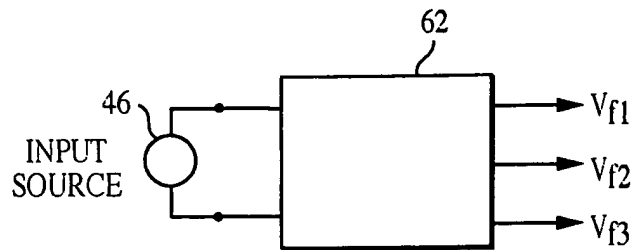


FIG. 8B

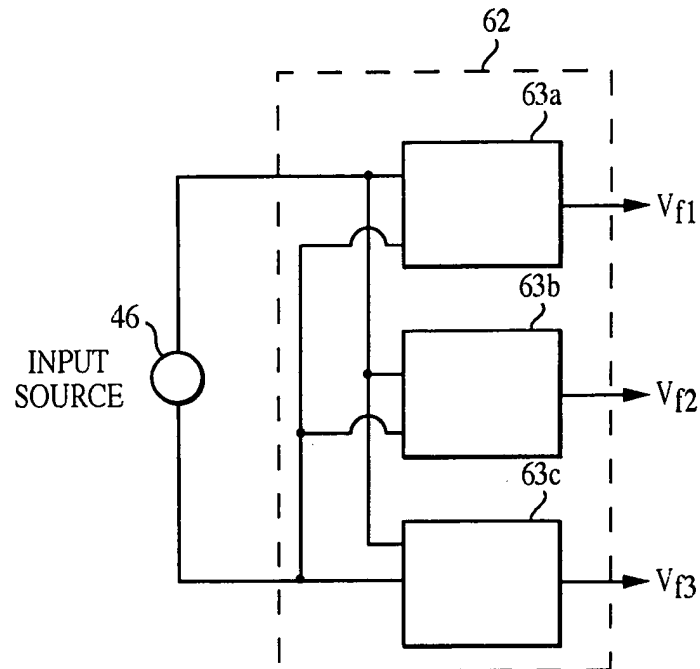
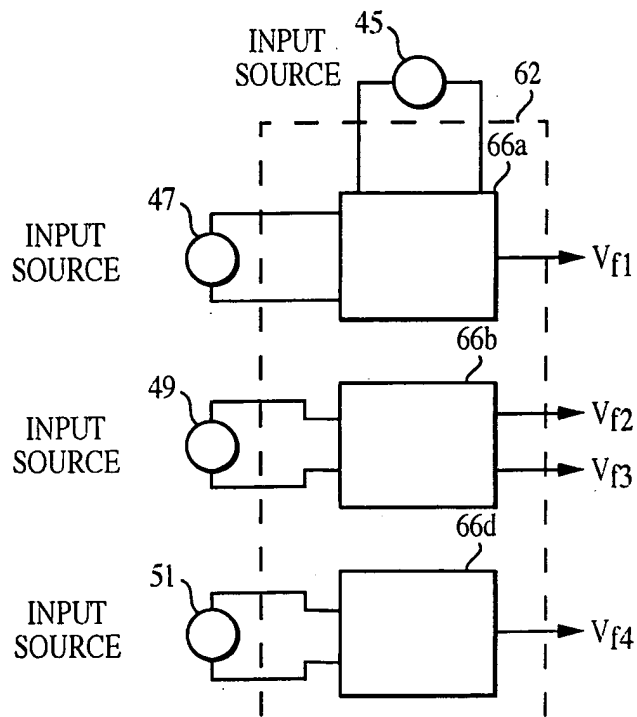


FIG. 8C



Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER CONVERTERS

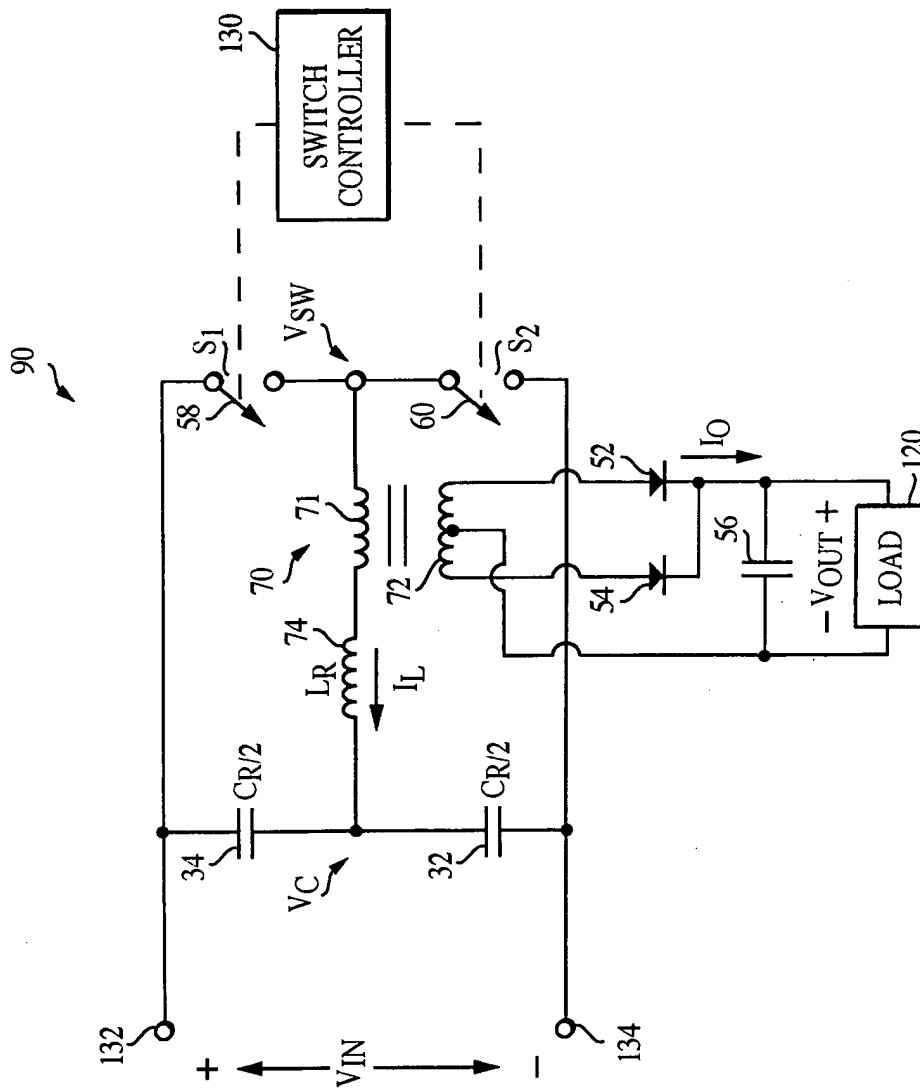
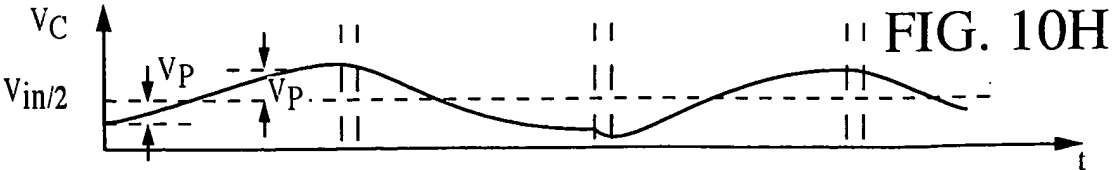
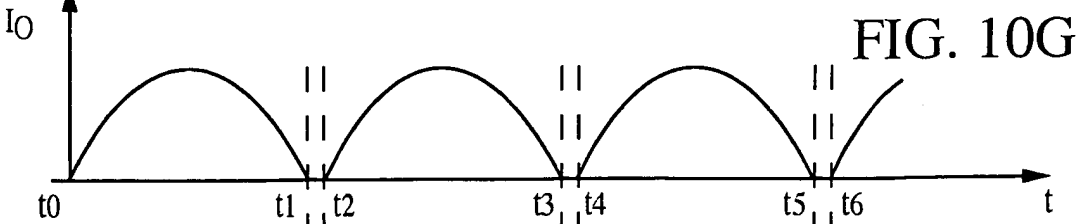
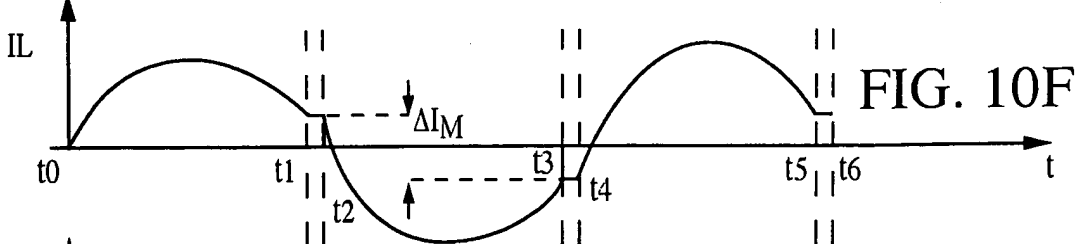
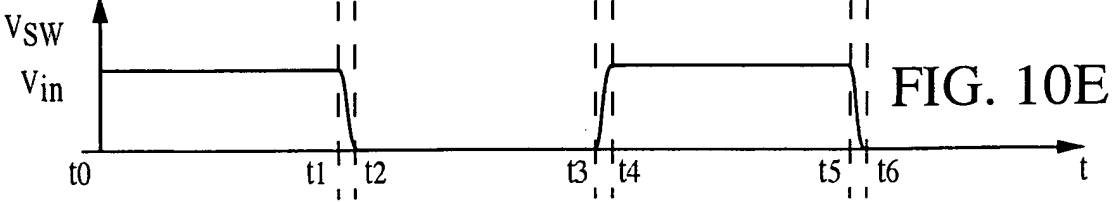
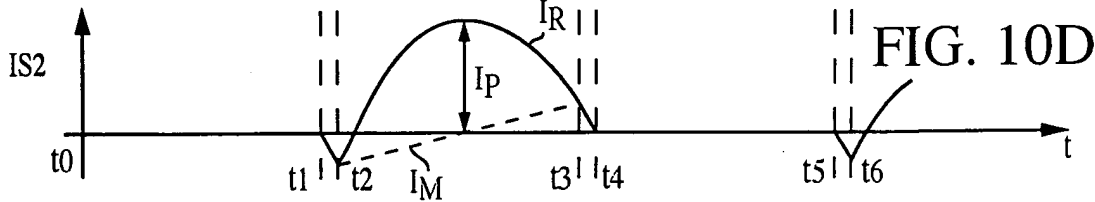
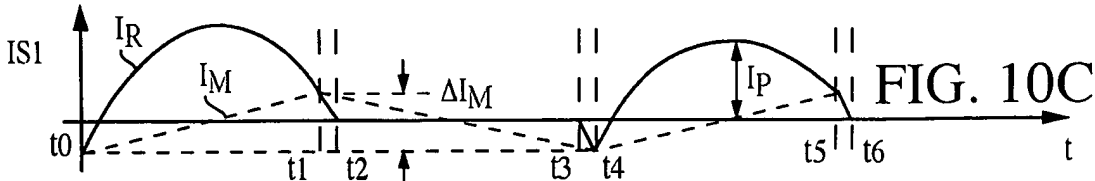
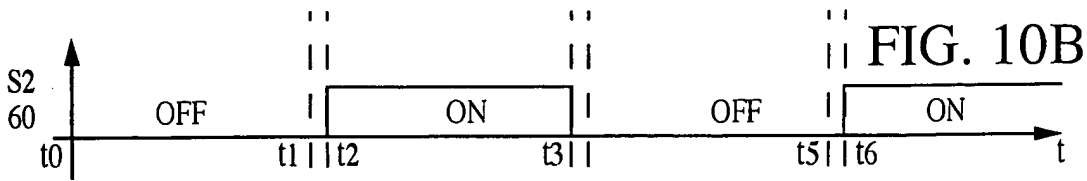
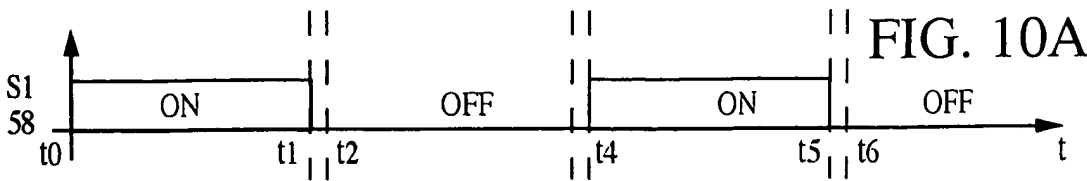


FIG. 9



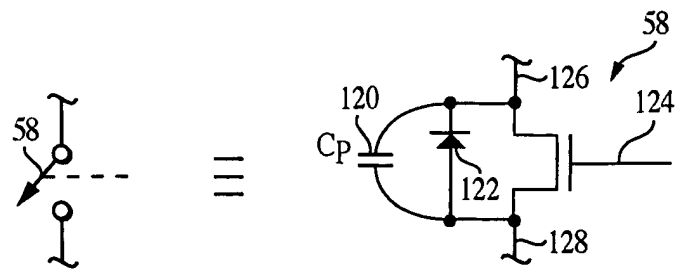


FIG. 11

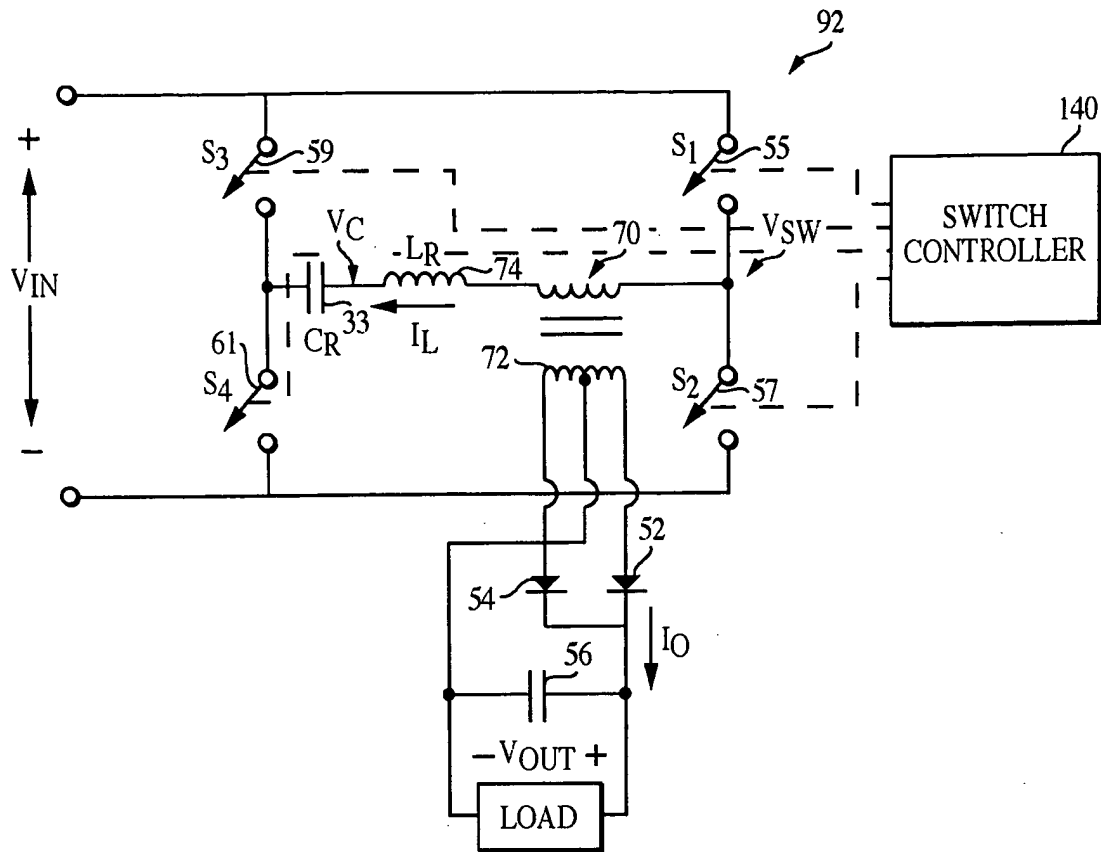


FIG. 12

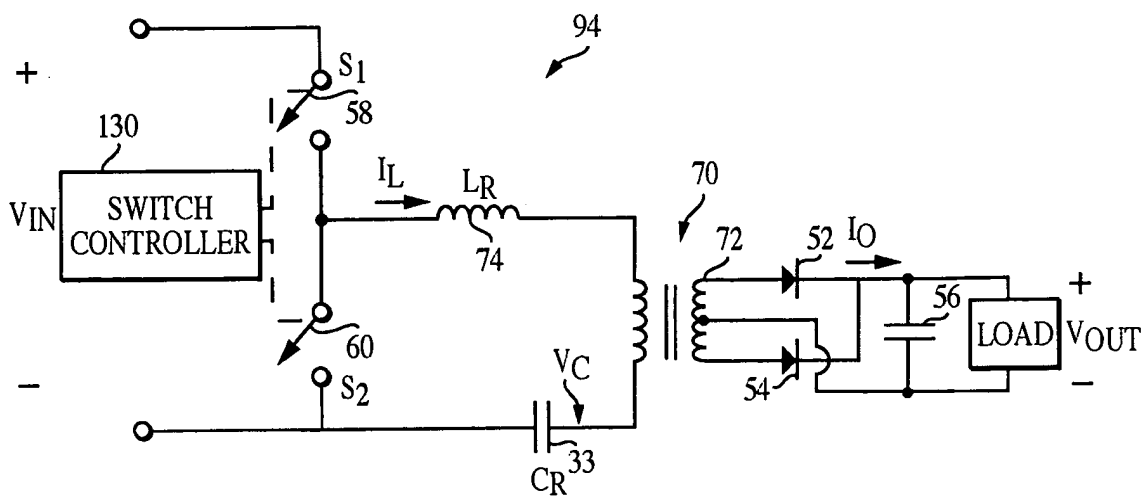


FIG. 13

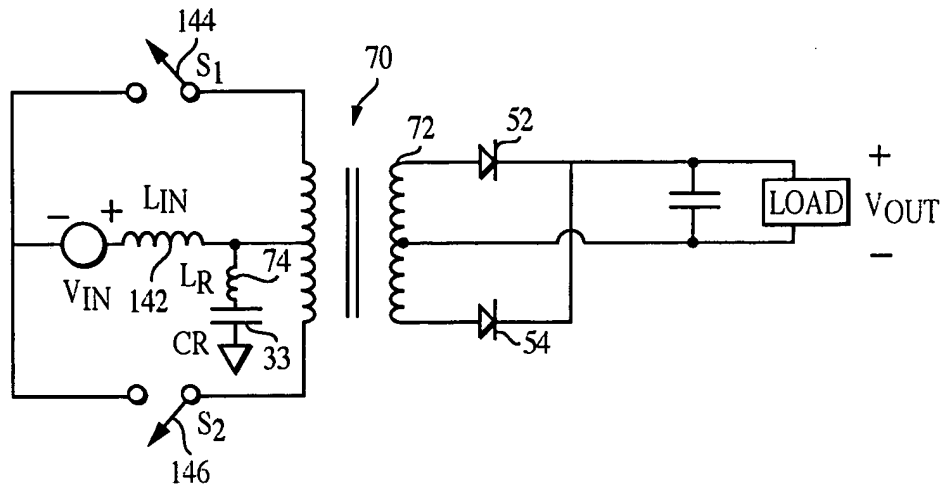


FIG. 14

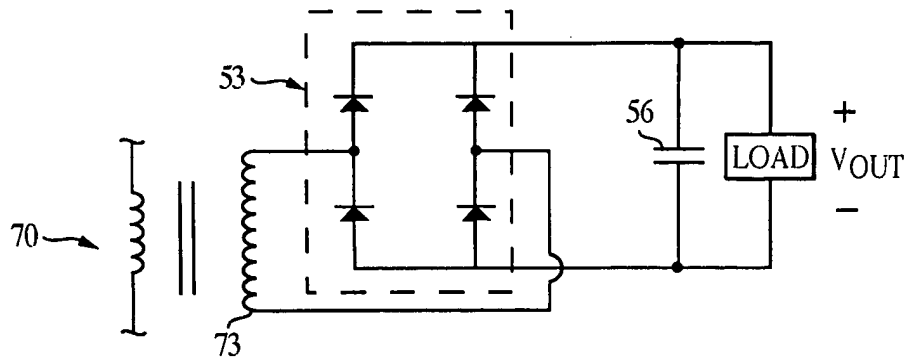


FIG. 15A

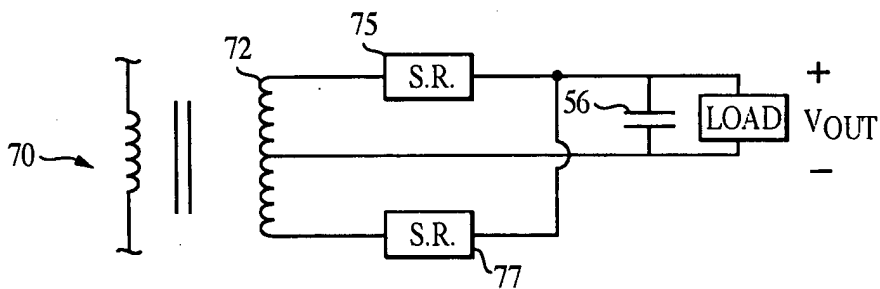


FIG. 15B

Applicant(s): Patrizio Vinciarelli

## OUTPUT RESISTANCE MODULATION IN POWER CONVERTERS

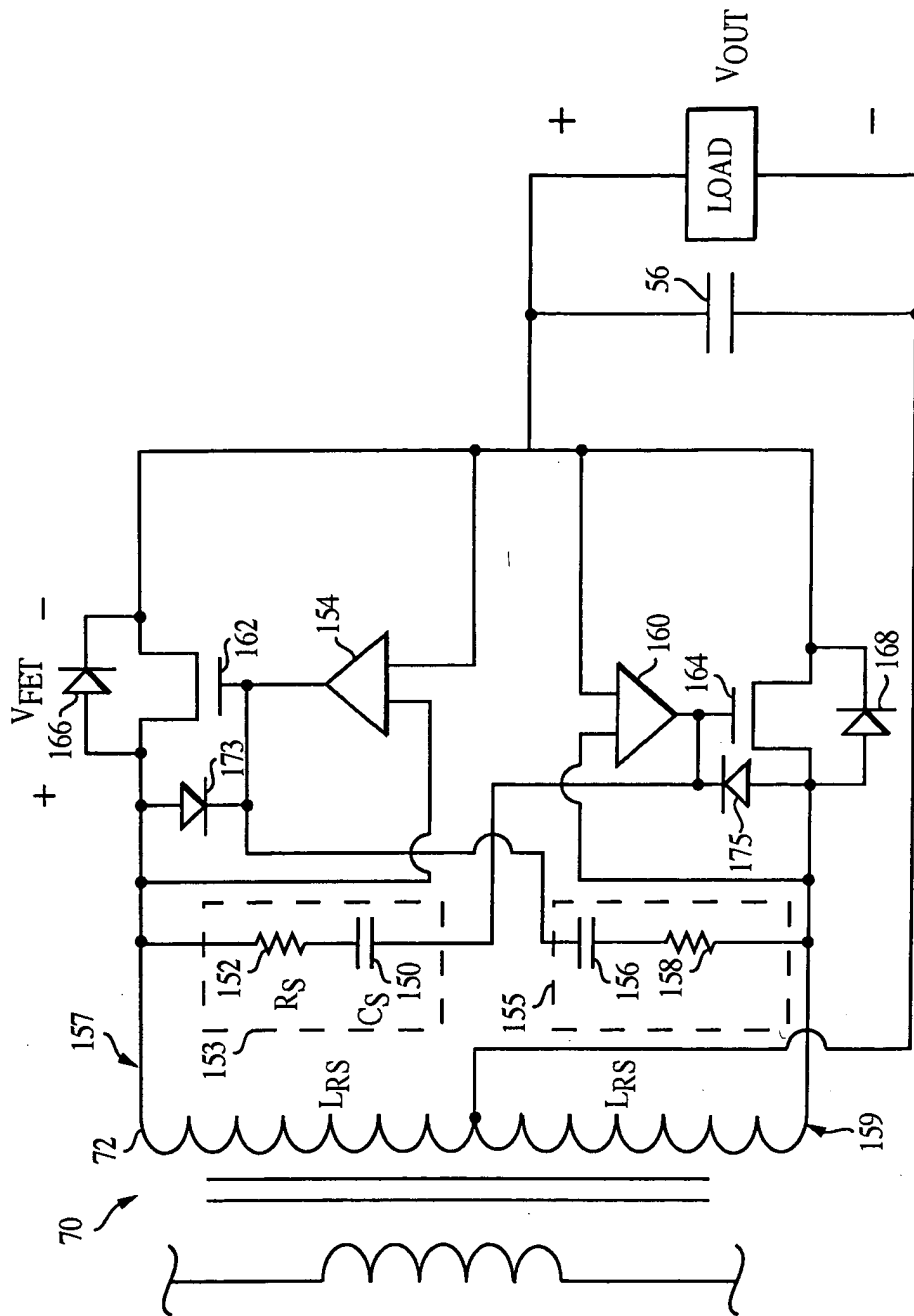


FIG. 15C



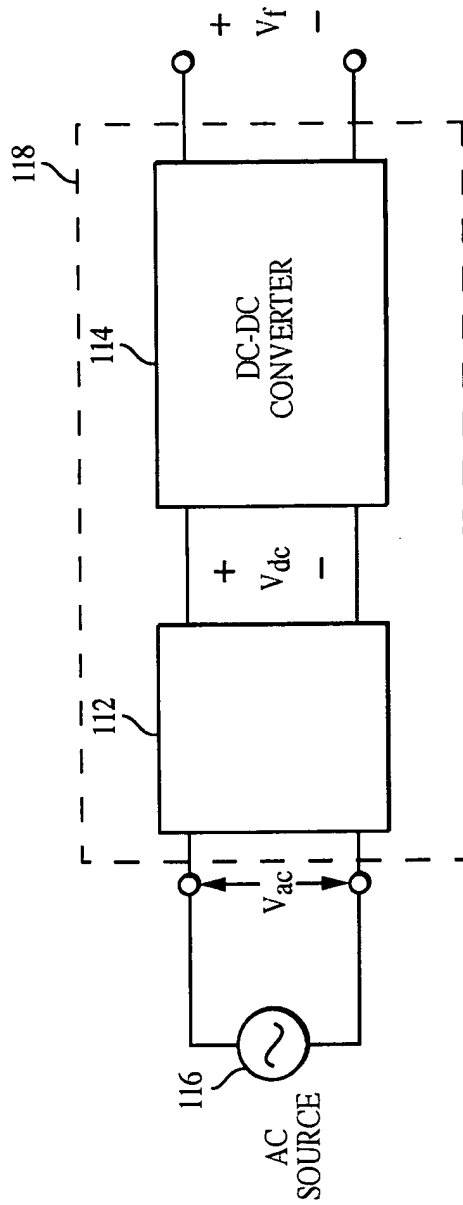


FIG. 16

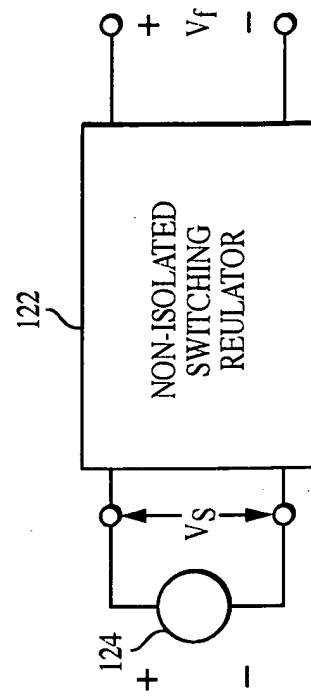


FIG. 17

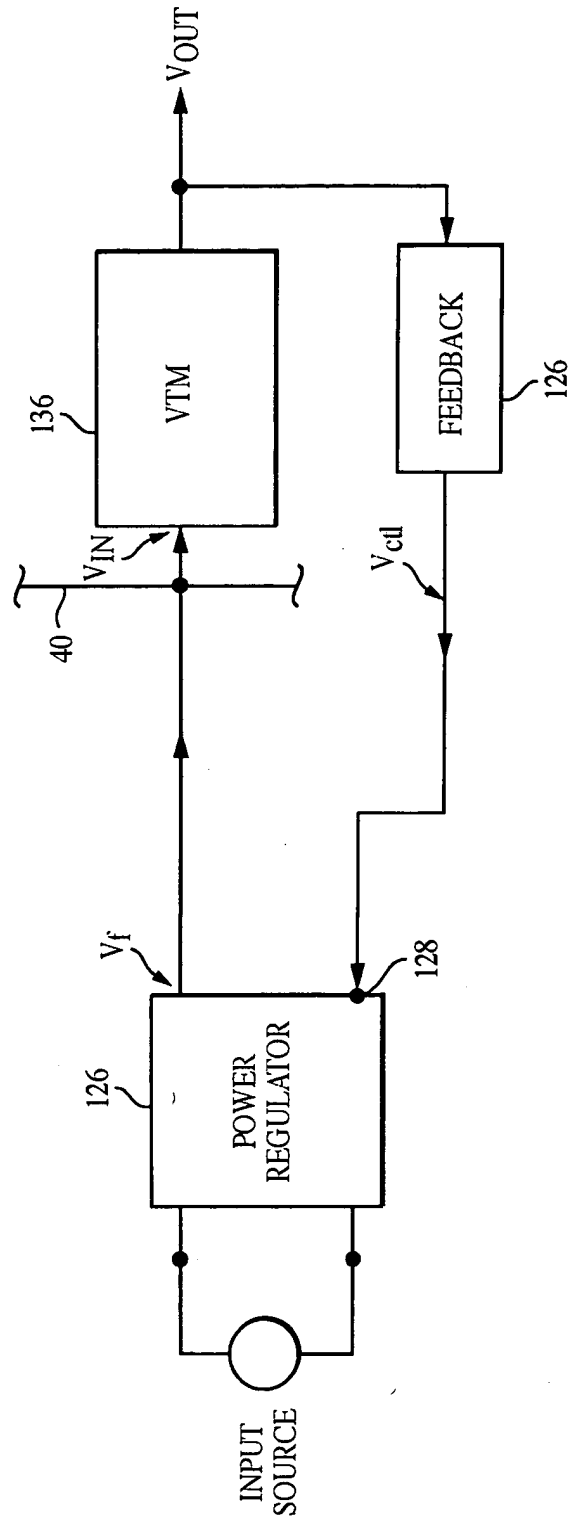


FIG. 18

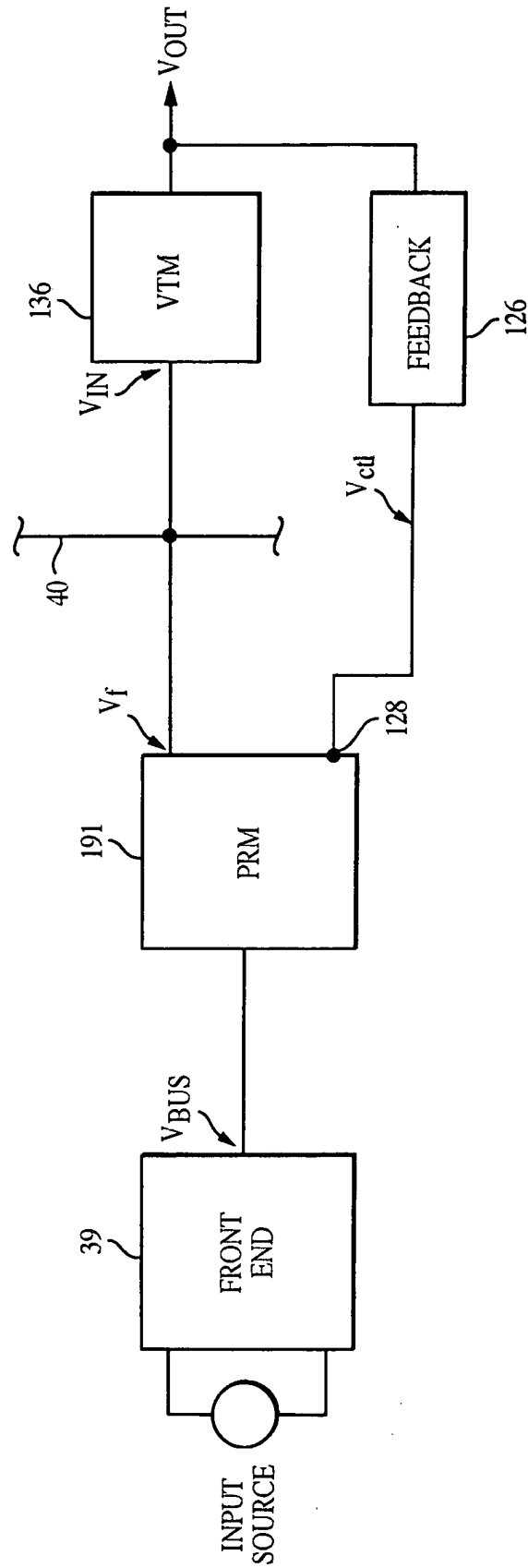


FIG. 19

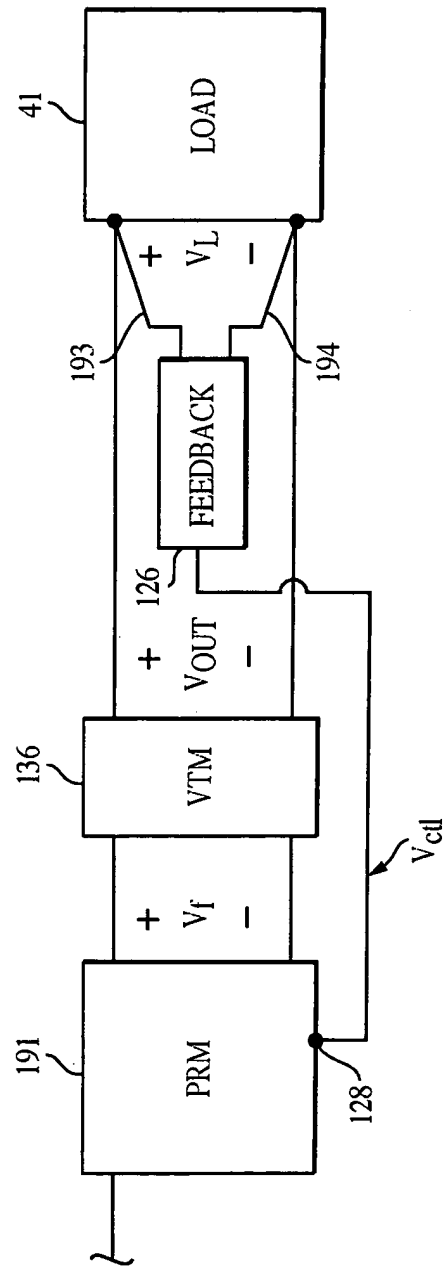


FIG. 20

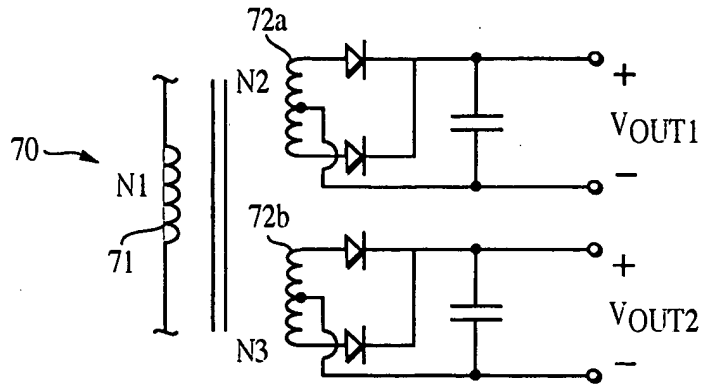


FIG. 21

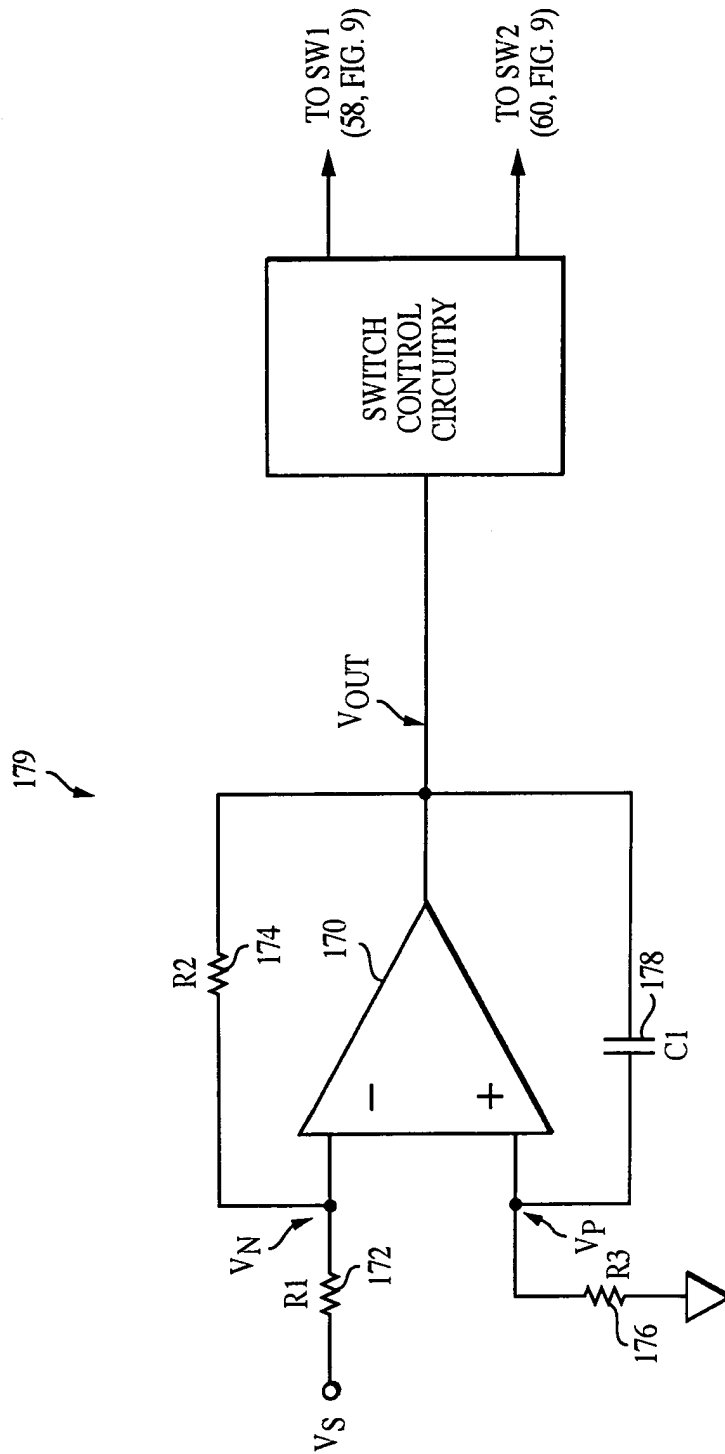
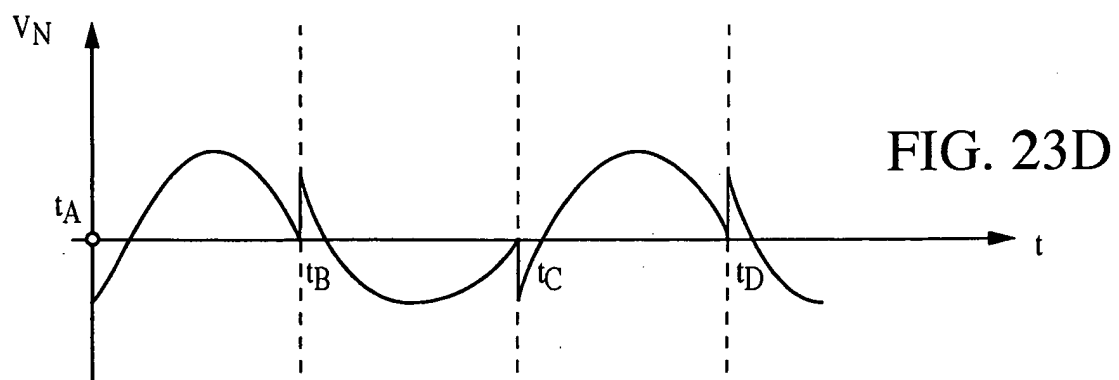
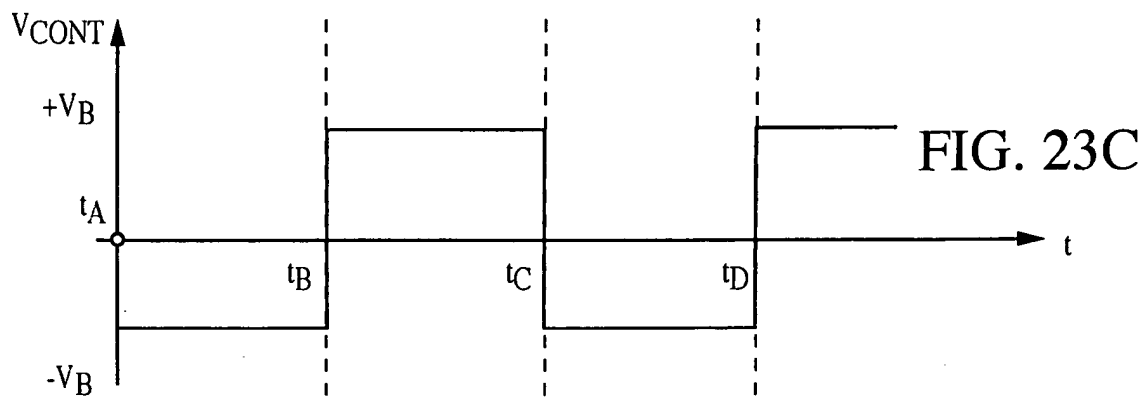
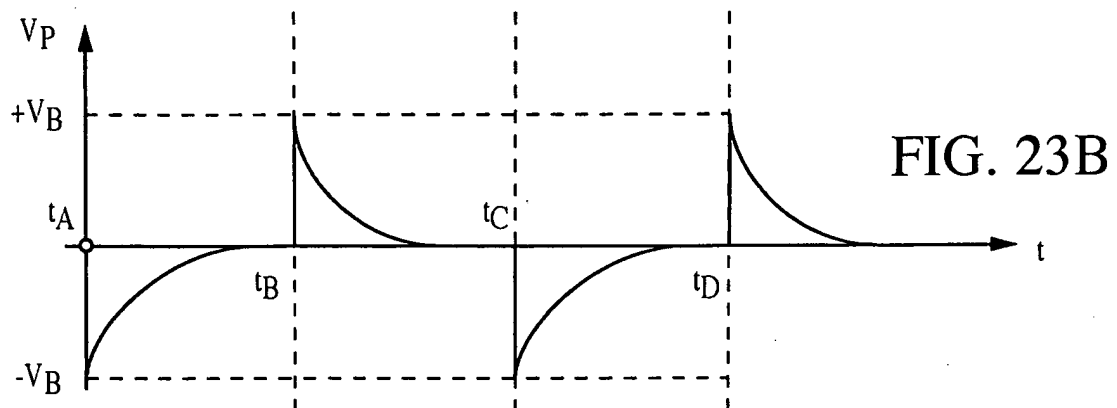
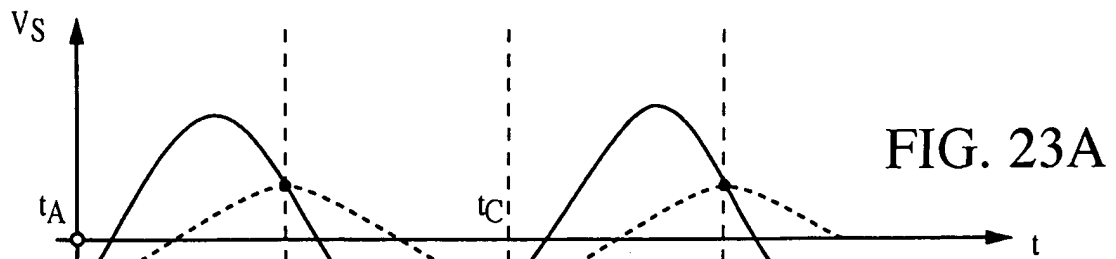


FIG. 22



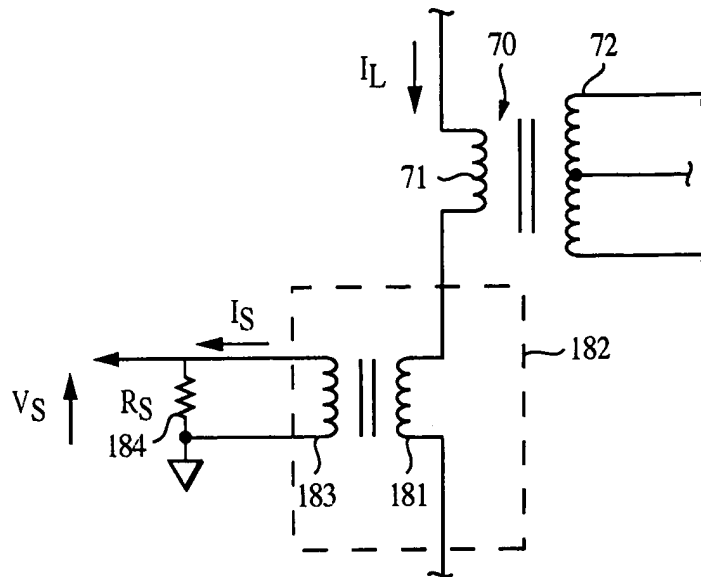


FIG. 24



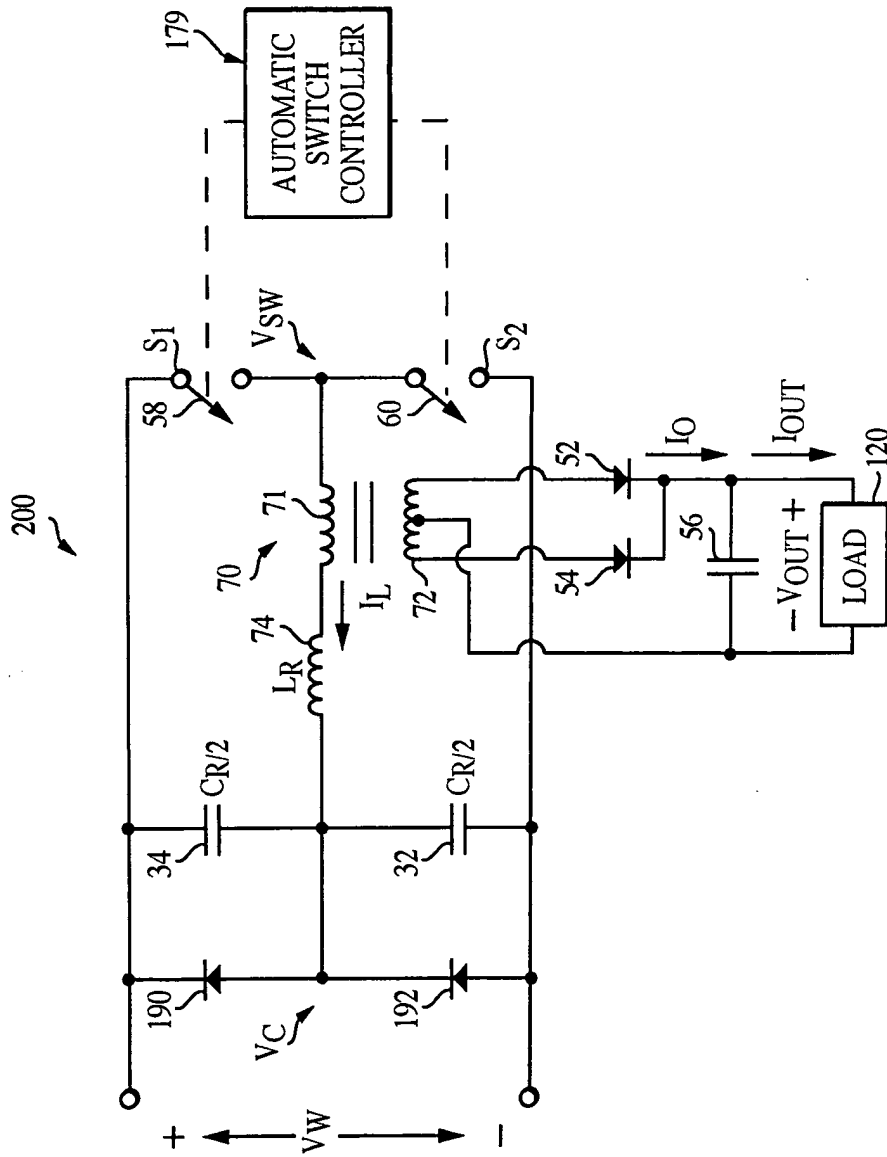


FIG. 25

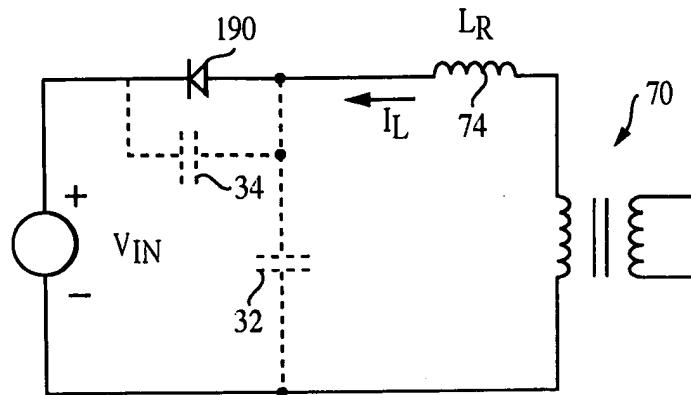


FIG. 26

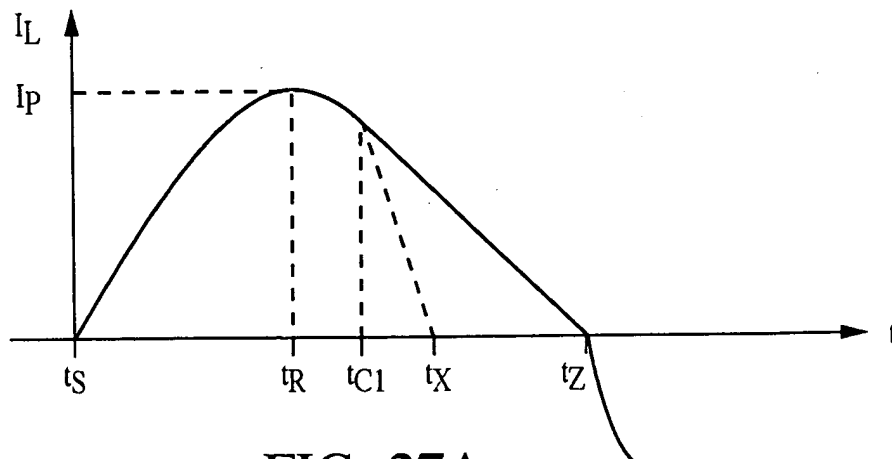


FIG. 27A

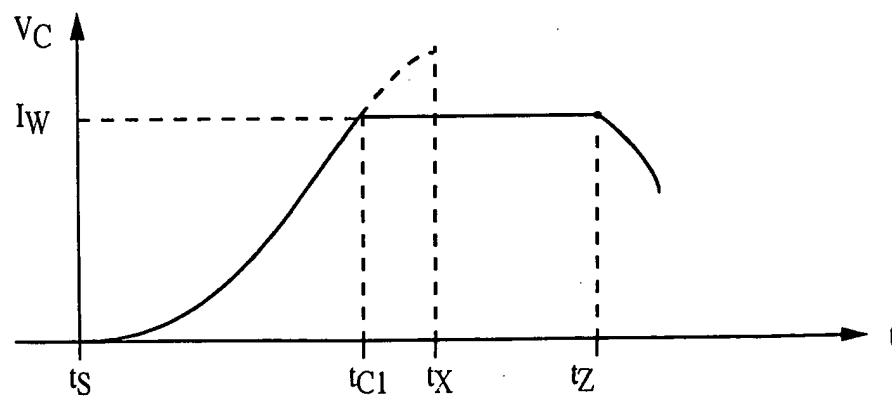


FIG. 27B

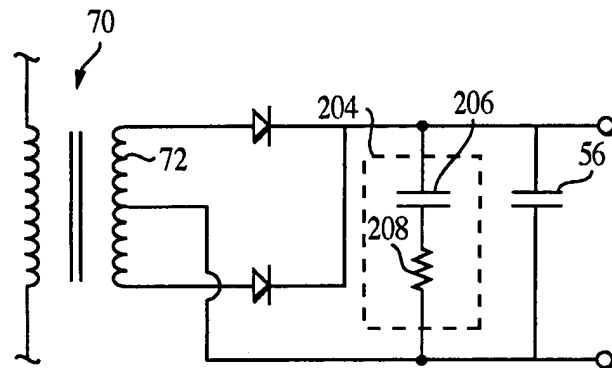
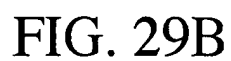
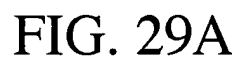


FIG. 28



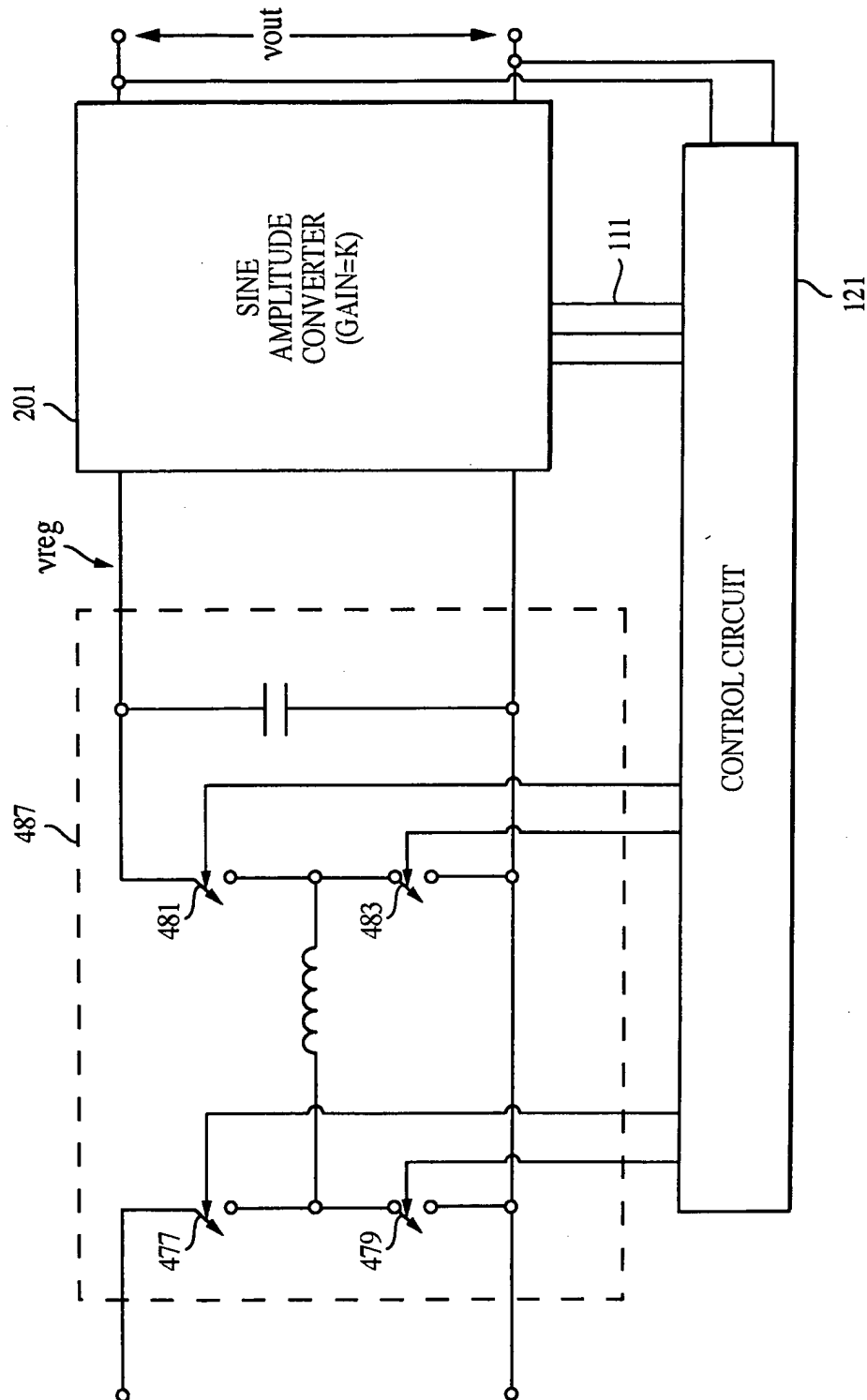


FIG. 29C

## BOOT STRAP REGULATED DC-DC CONVERTER

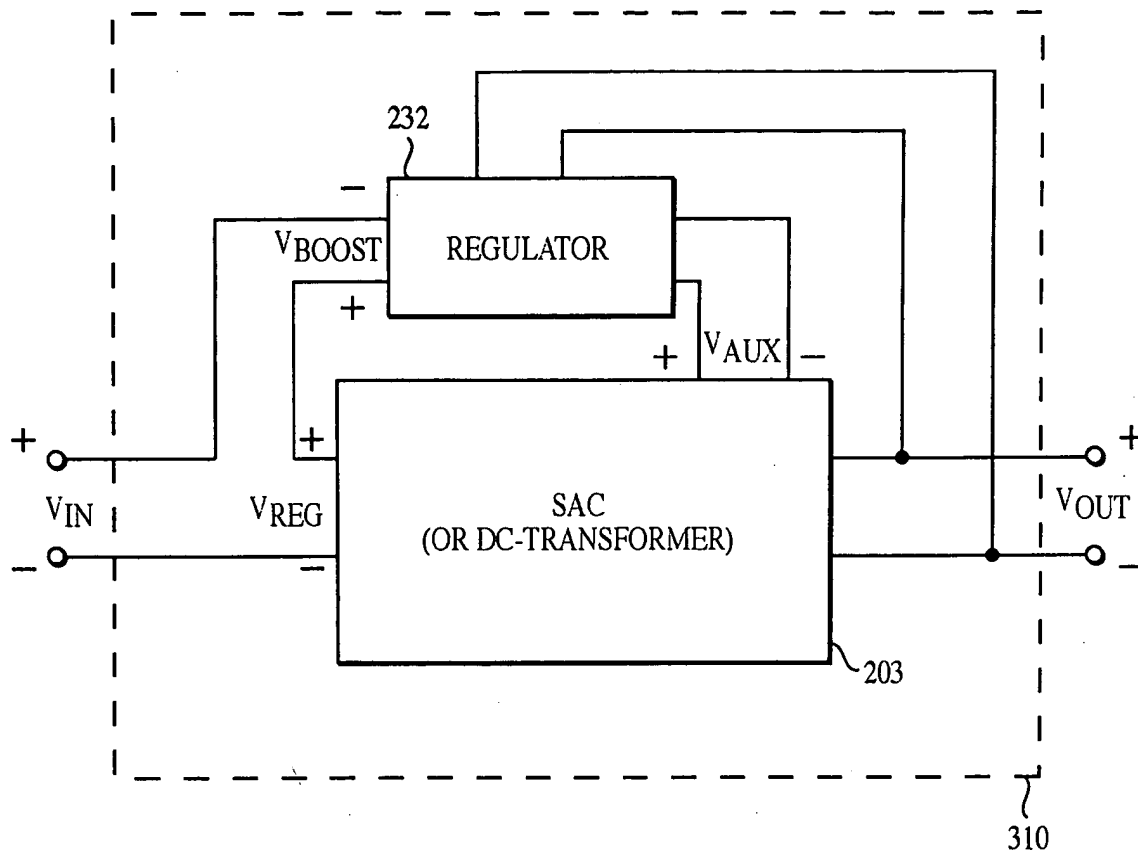


FIG. 30

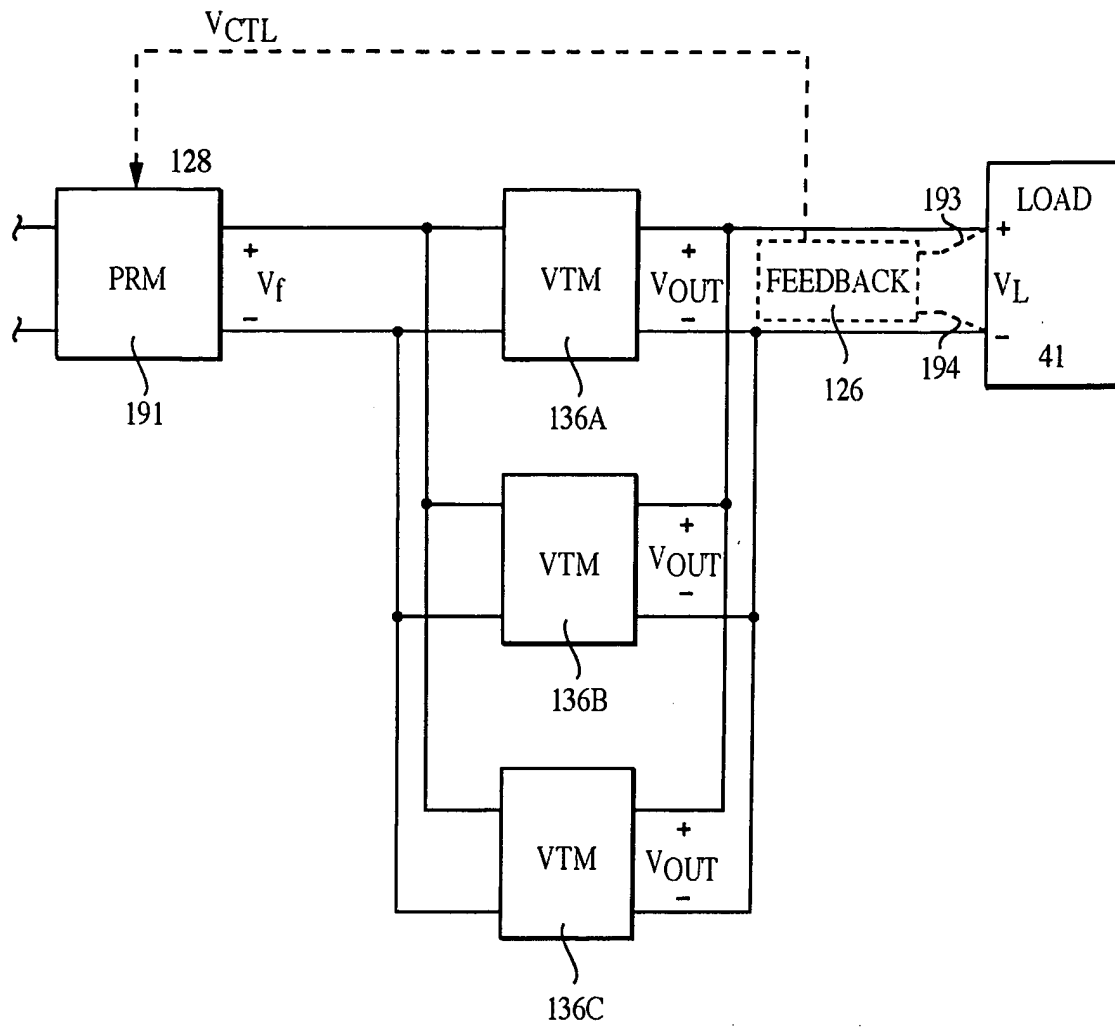


FIG. 31

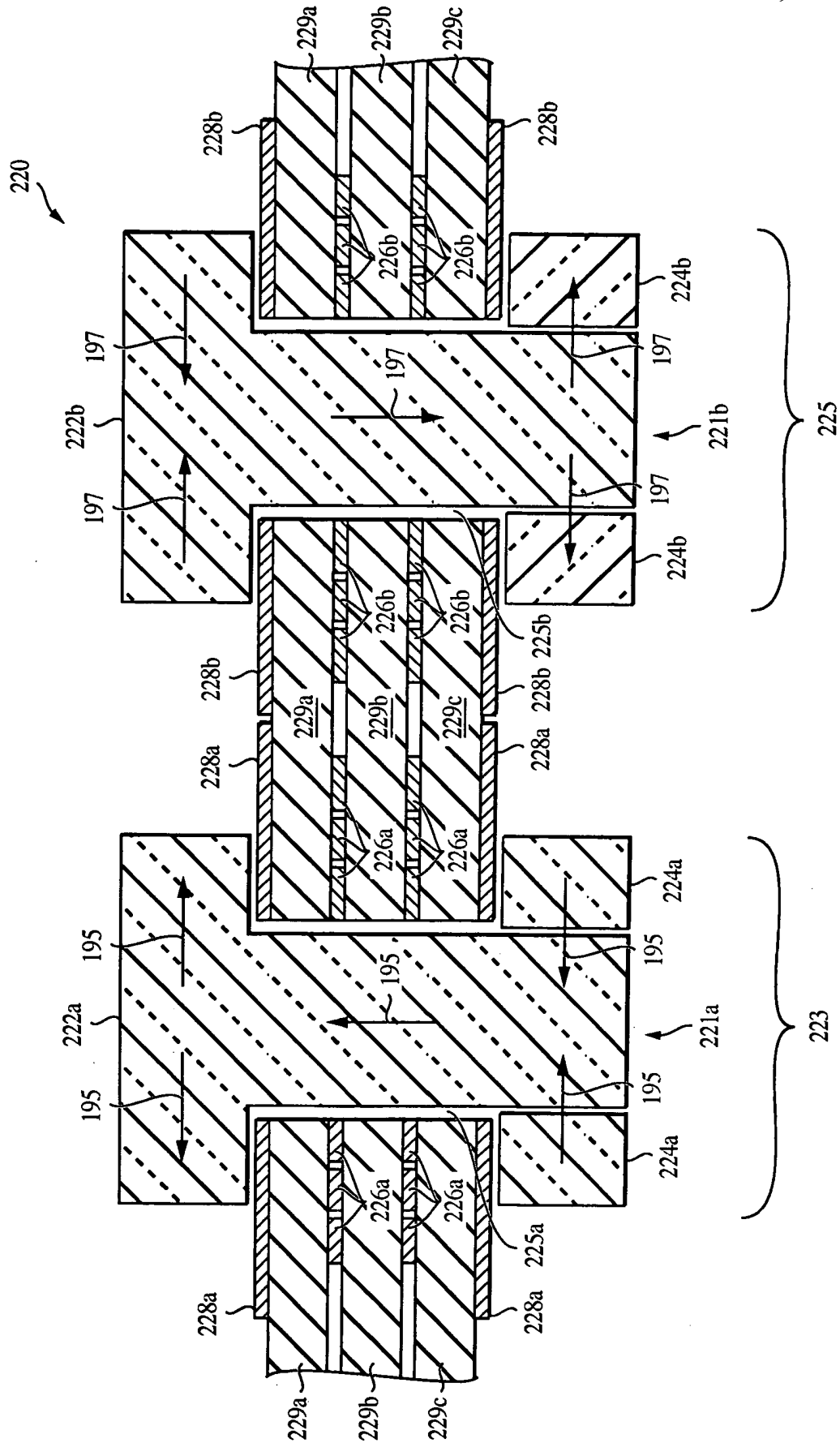


FIG. 32



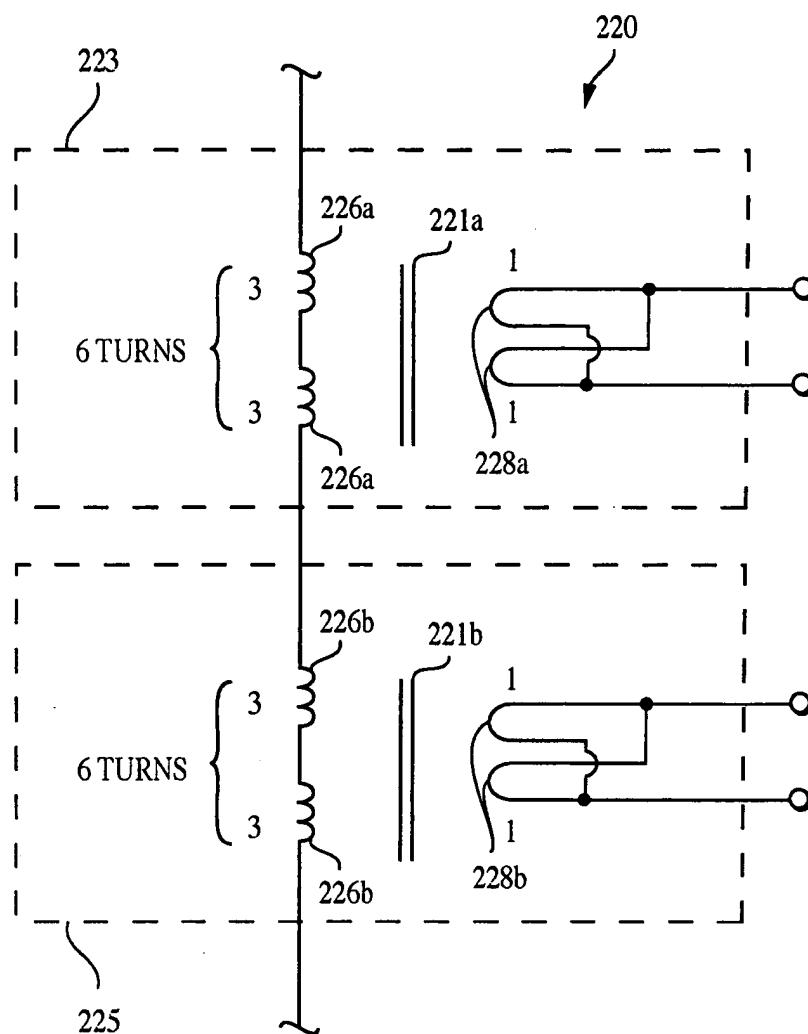


FIG. 33

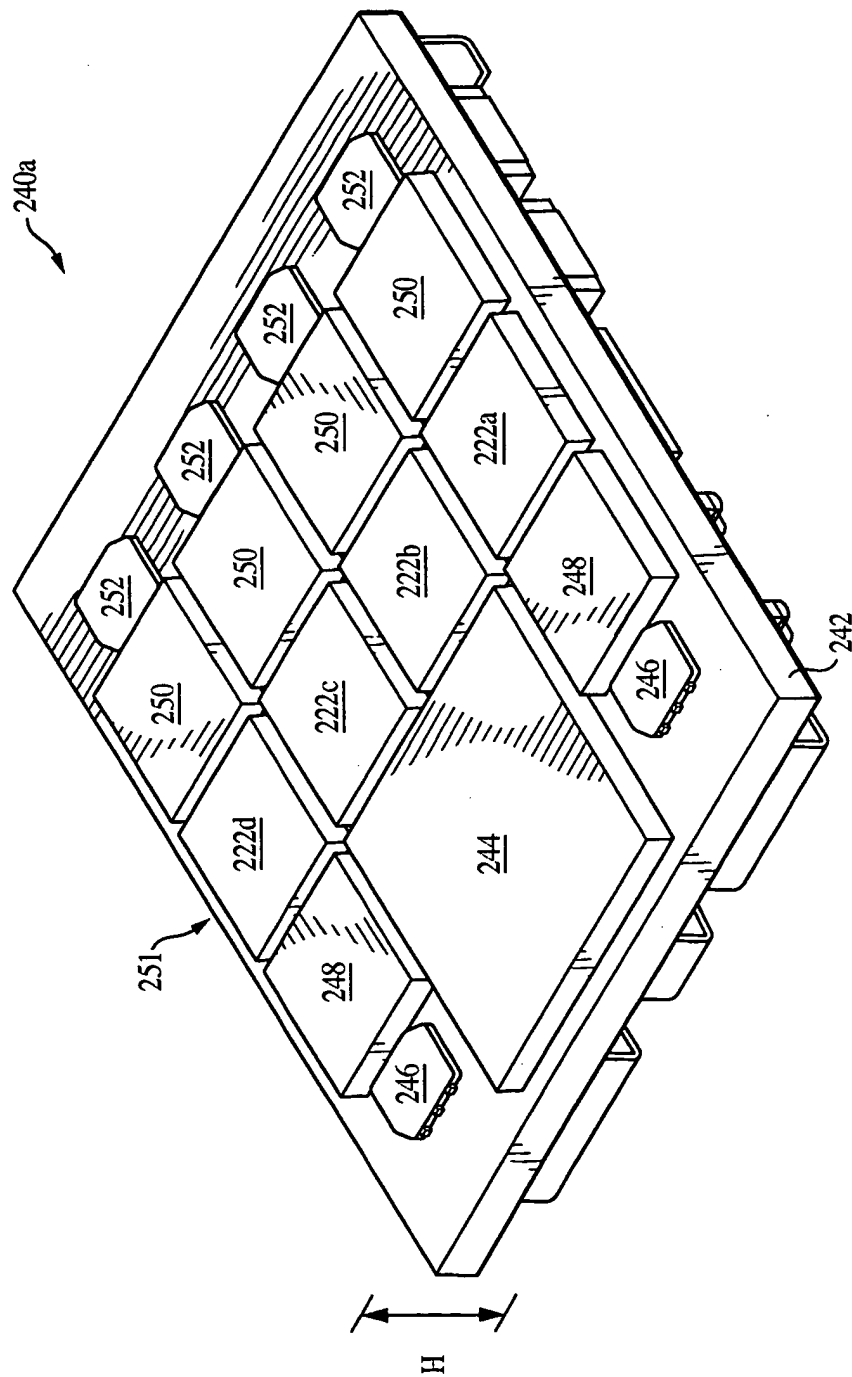


FIG. 34A

Applicant(s): Patrizio Vinciarelli  
 OUTPUT RESISTANCE MODULATION IN POWER  
 CONVERTERS

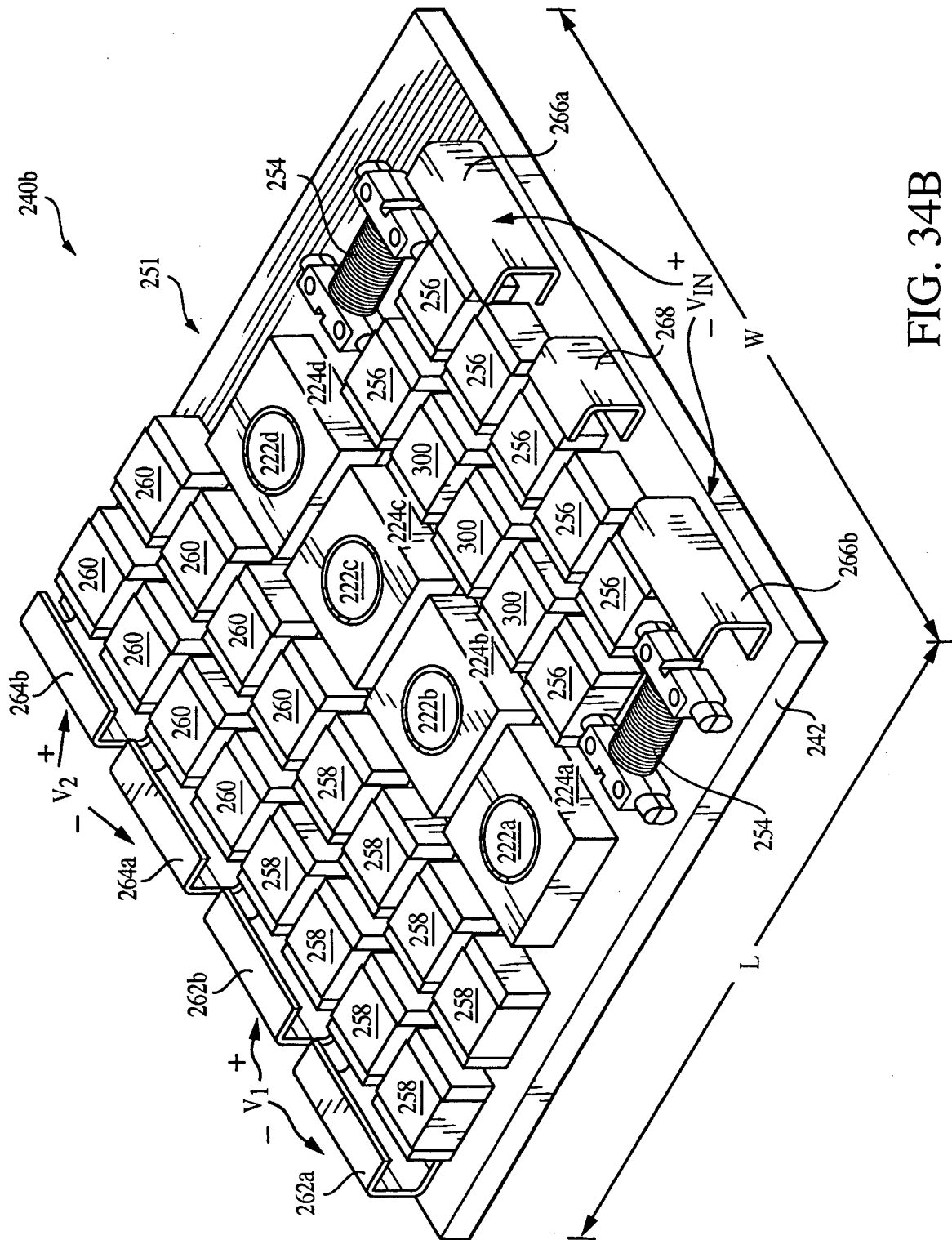
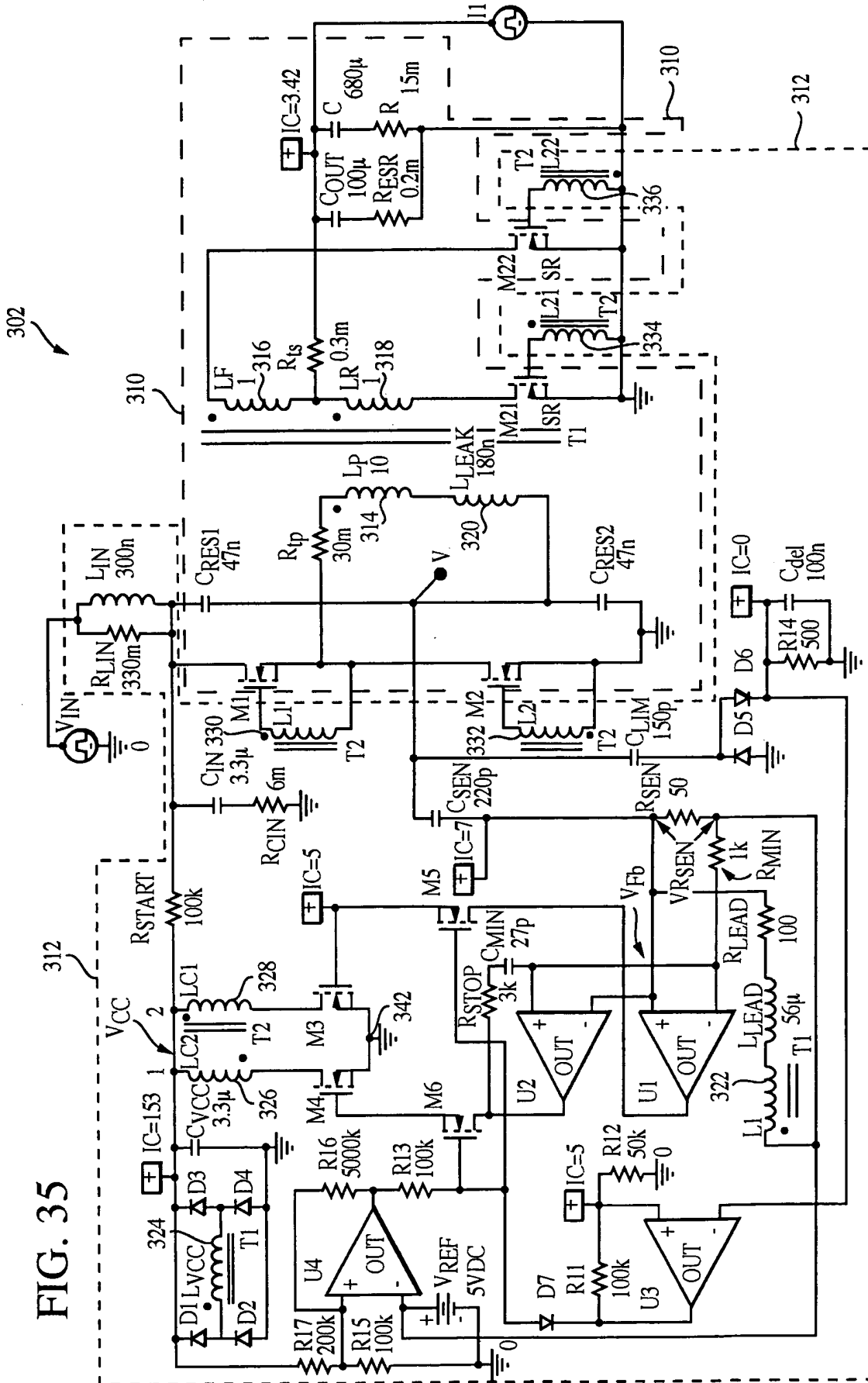
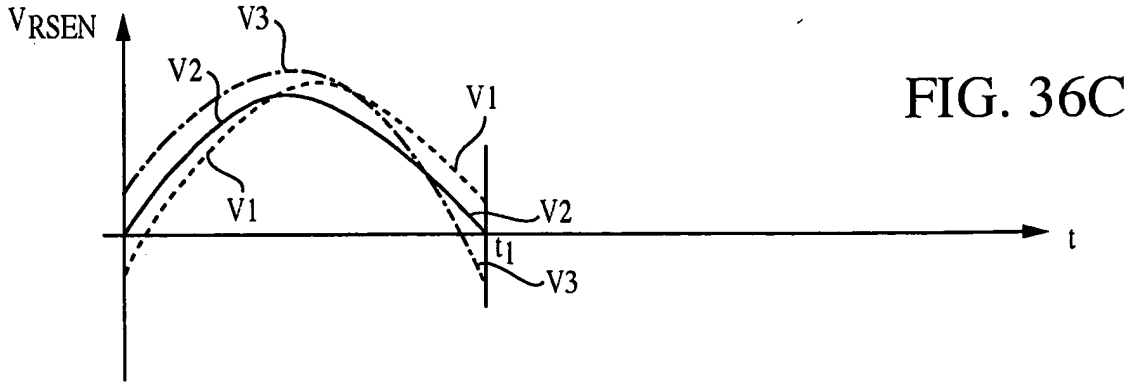
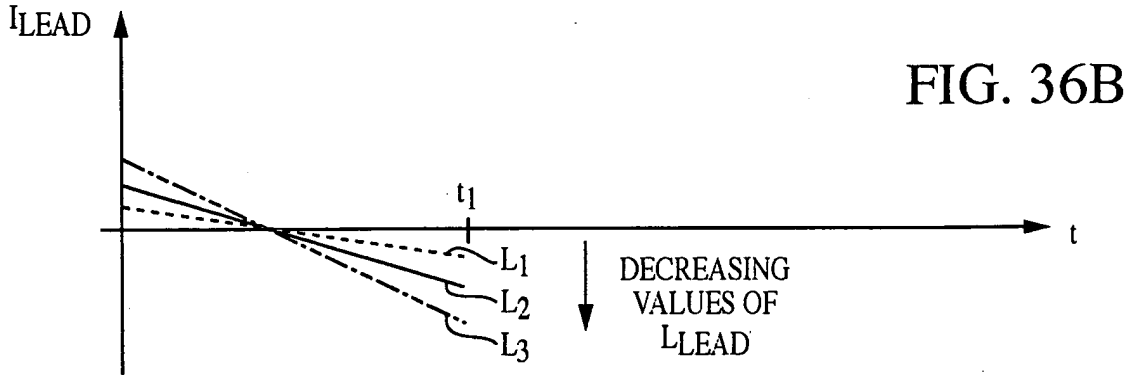
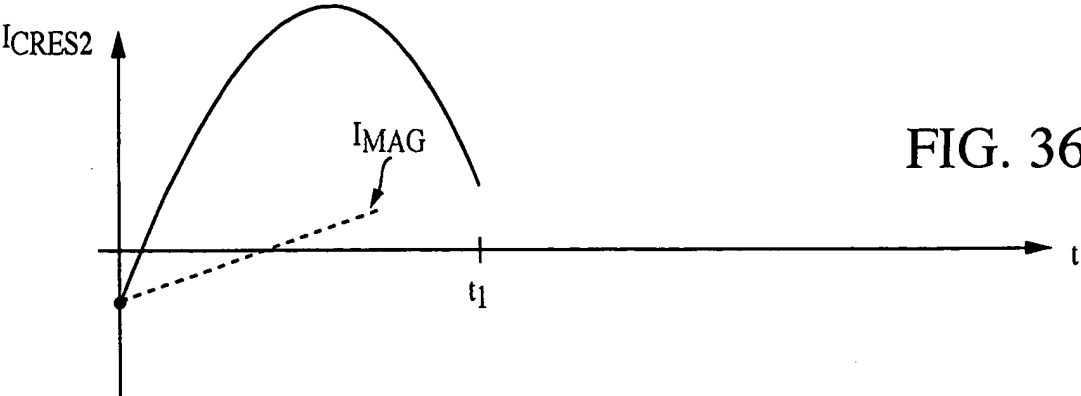


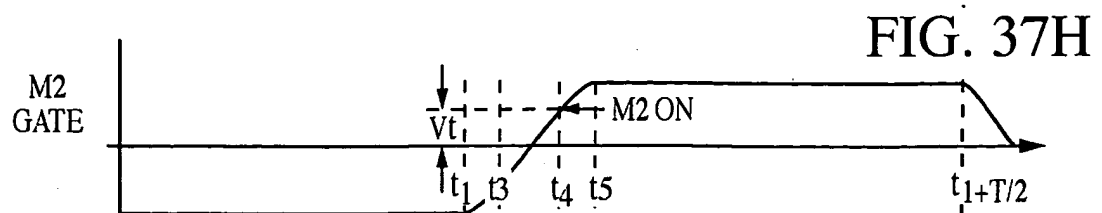
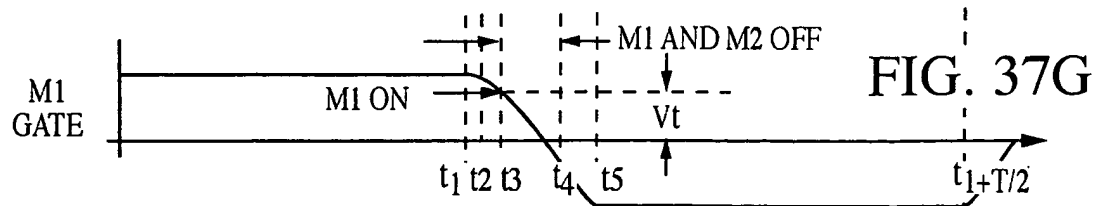
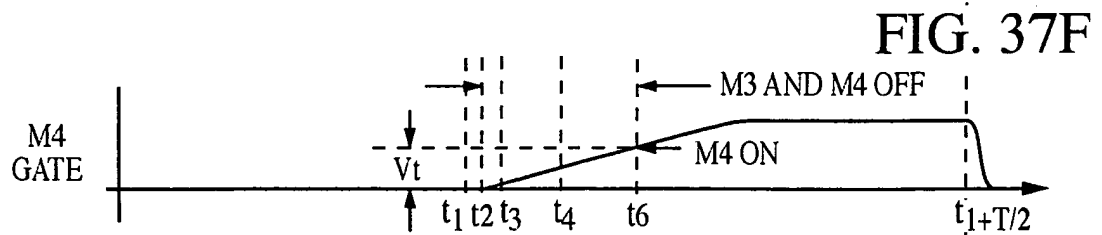
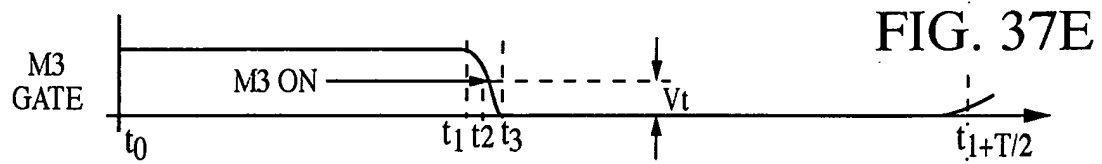
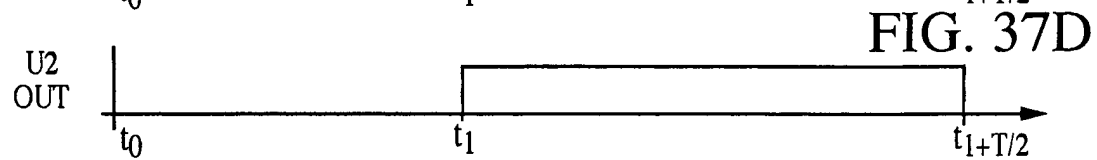
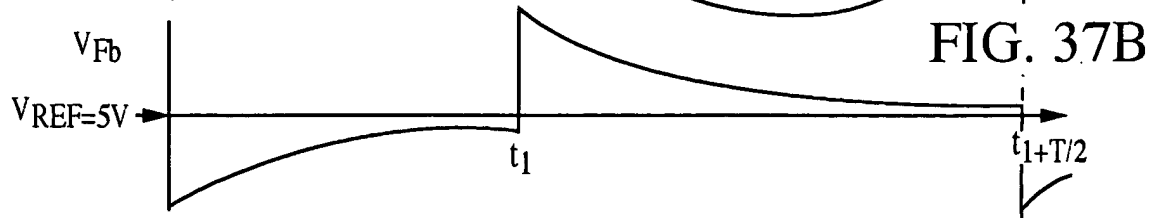
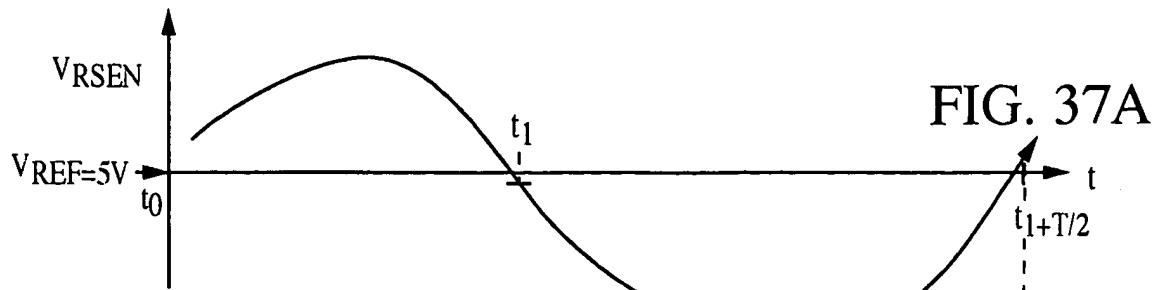
FIG. 34B

Applicant(s): Patrizio Vinciarelli

## OUTPUT RESISTANCE MODULATION IN POWER CONVERTERS







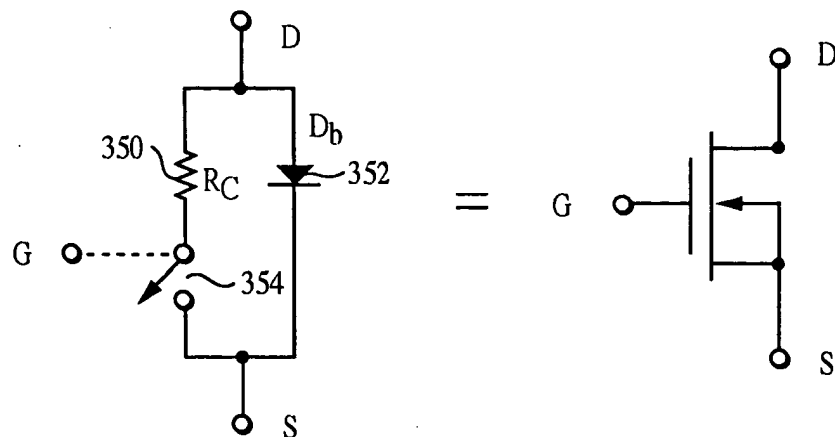


FIG. 38

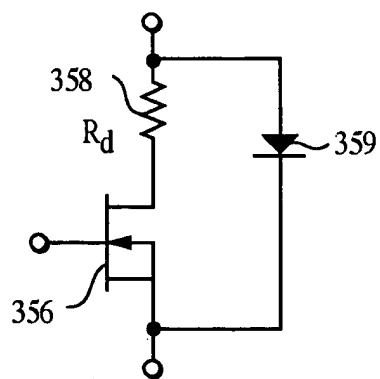


FIG. 39A

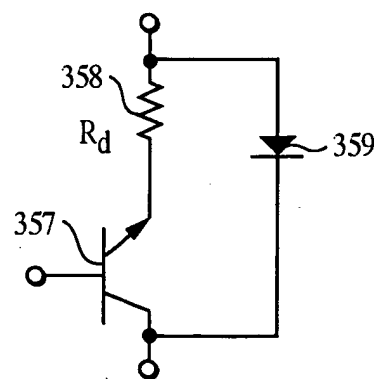


FIG. 39B

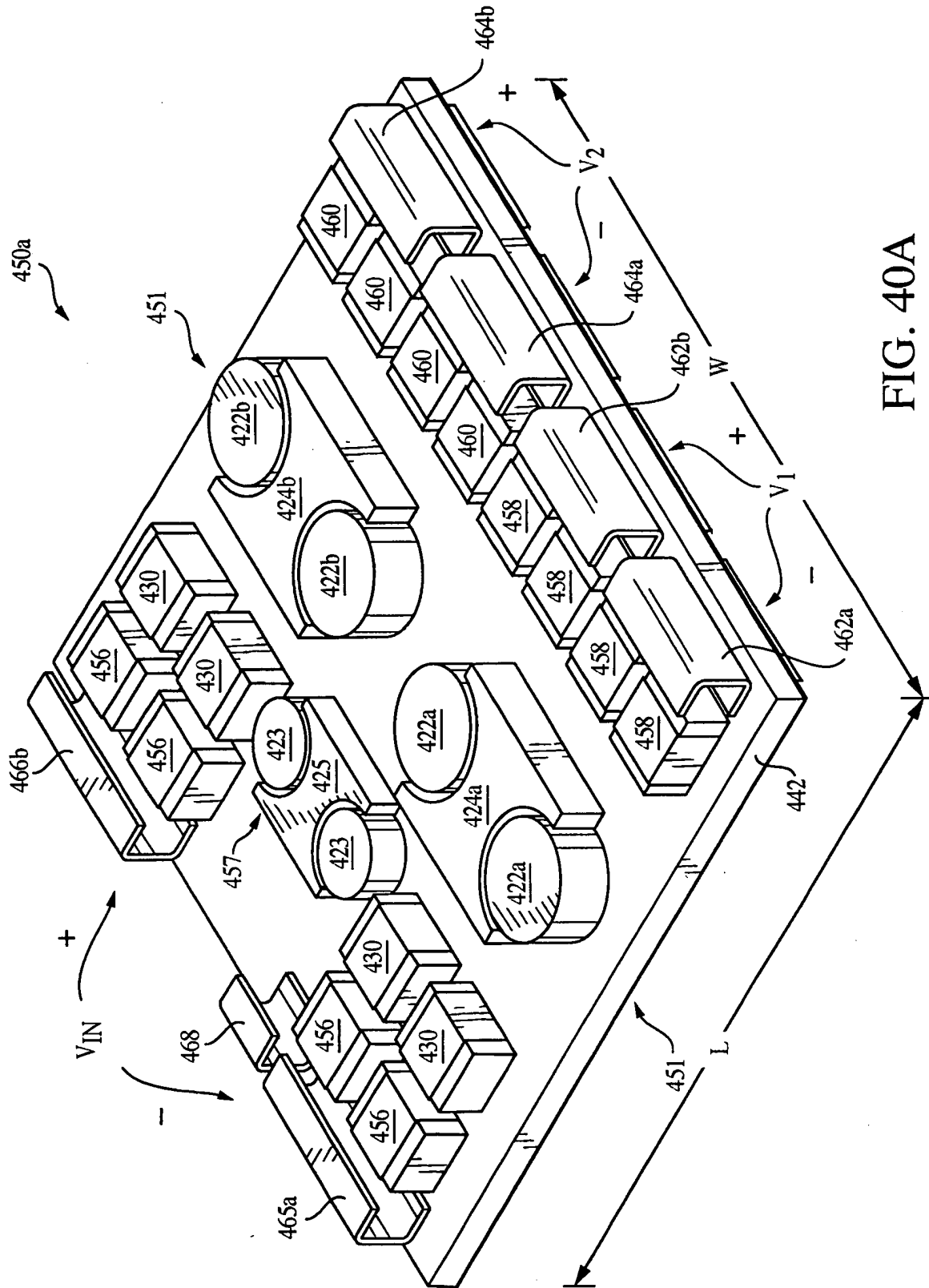


FIG. 40A



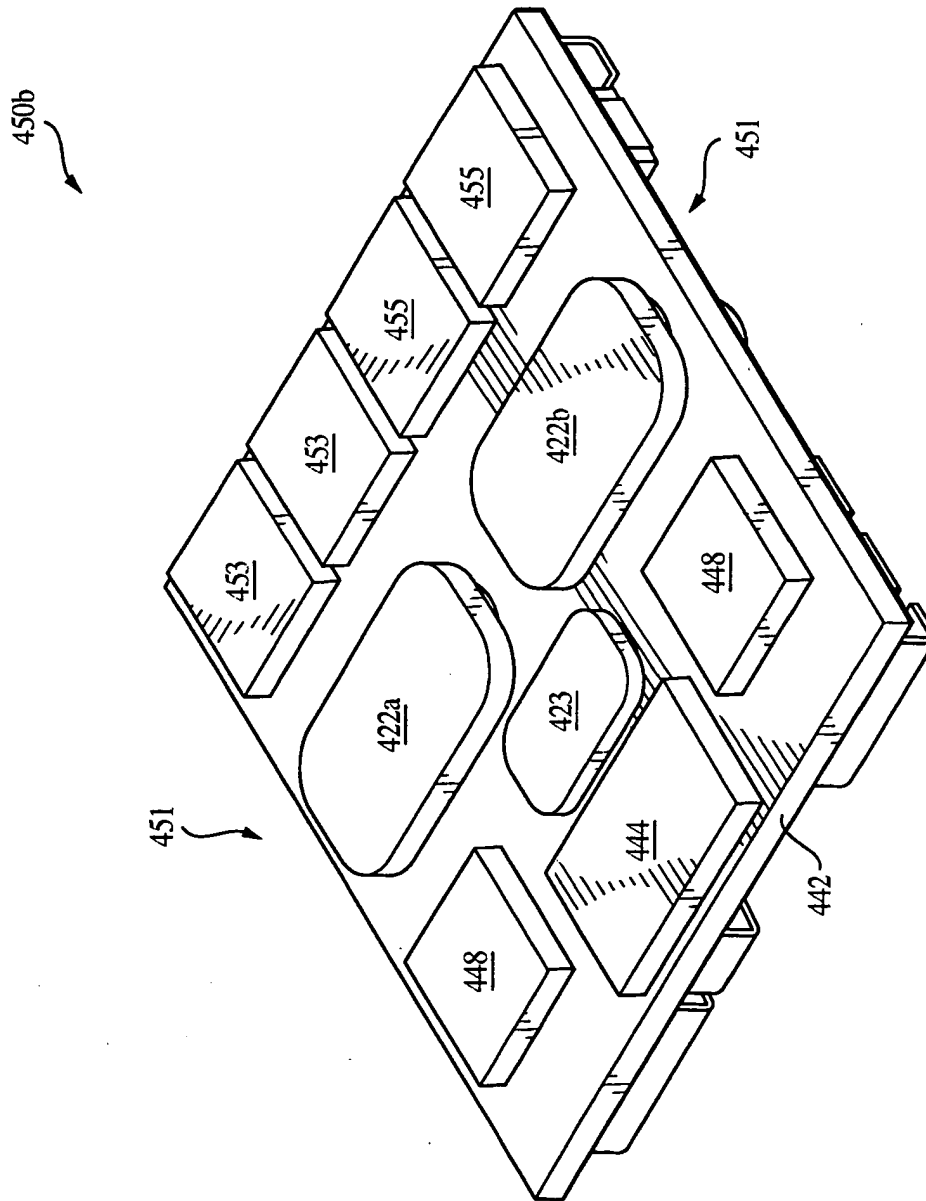


FIG. 40B

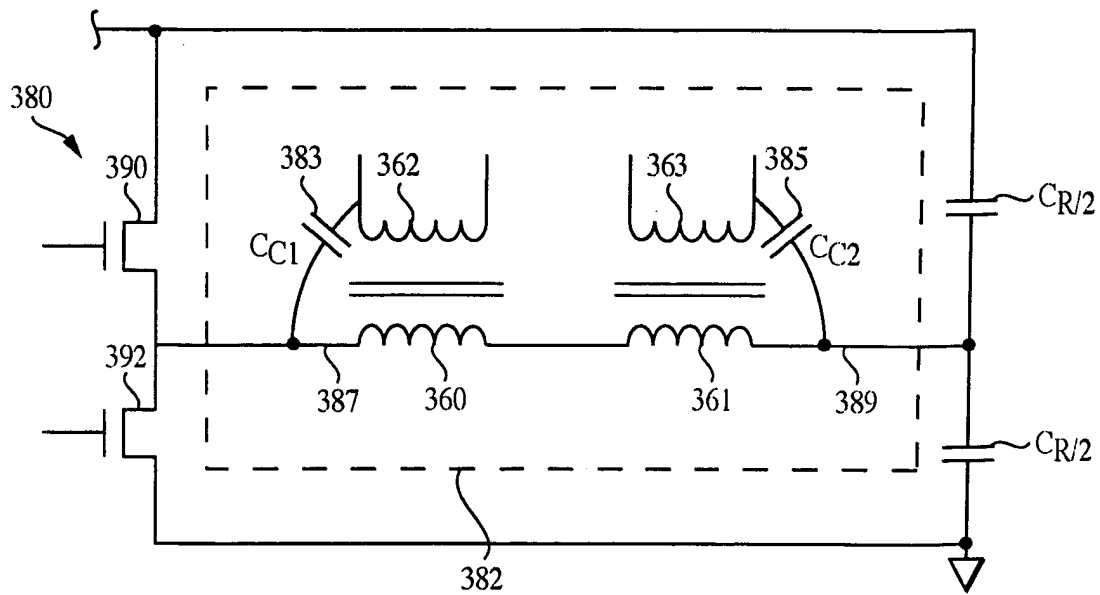


FIG. 41

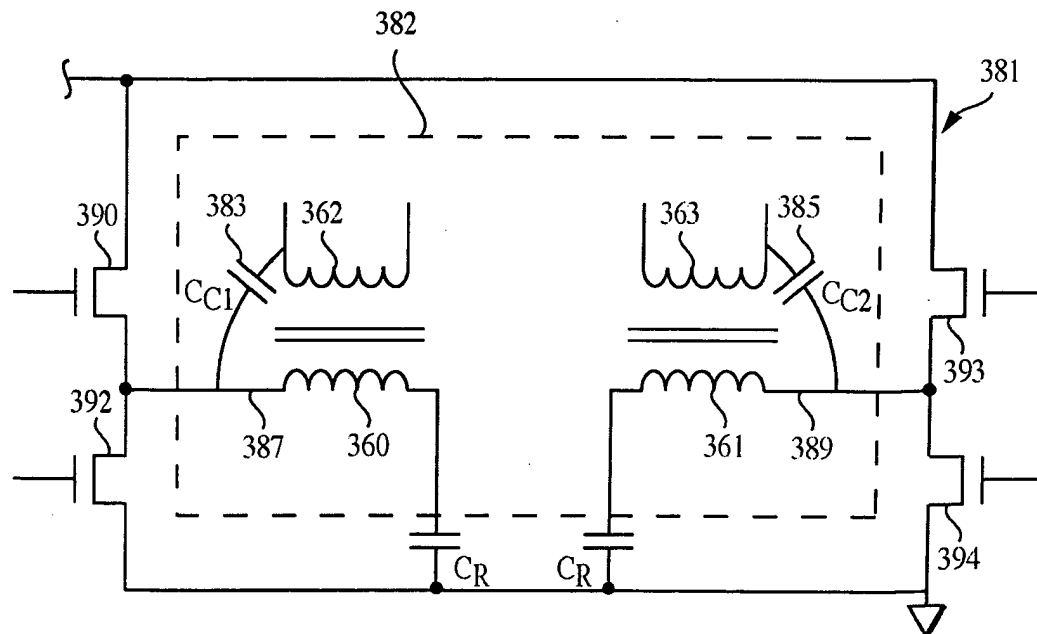


FIG. 42



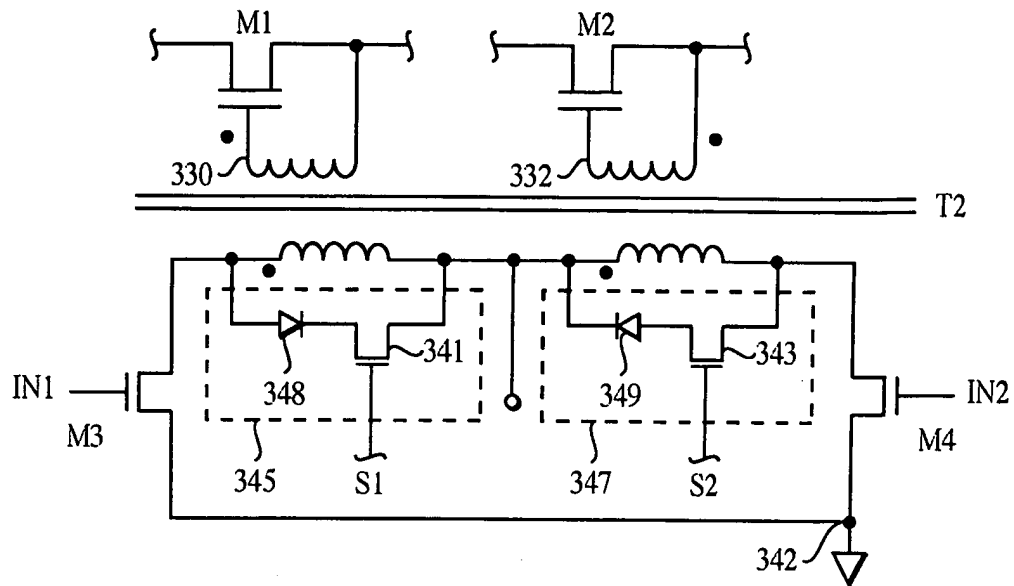


FIG. 45

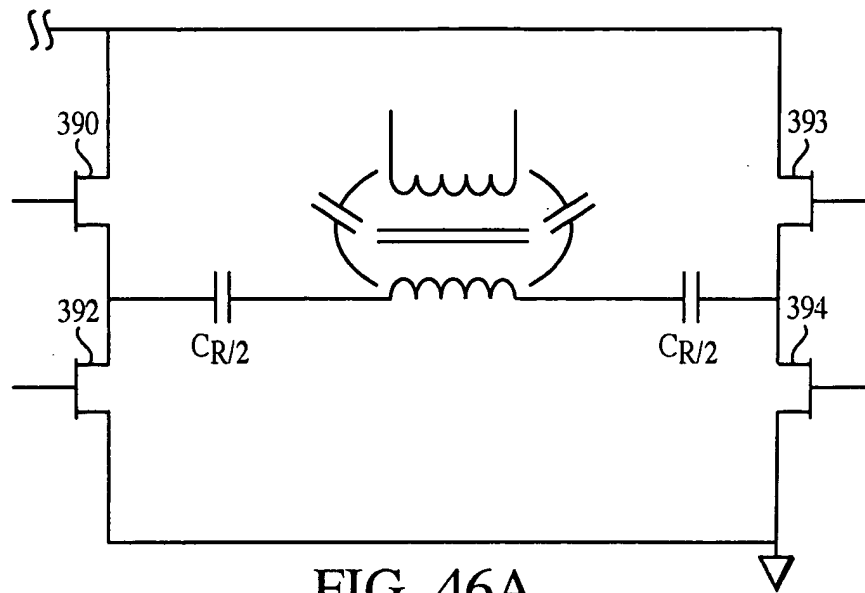


FIG. 46A

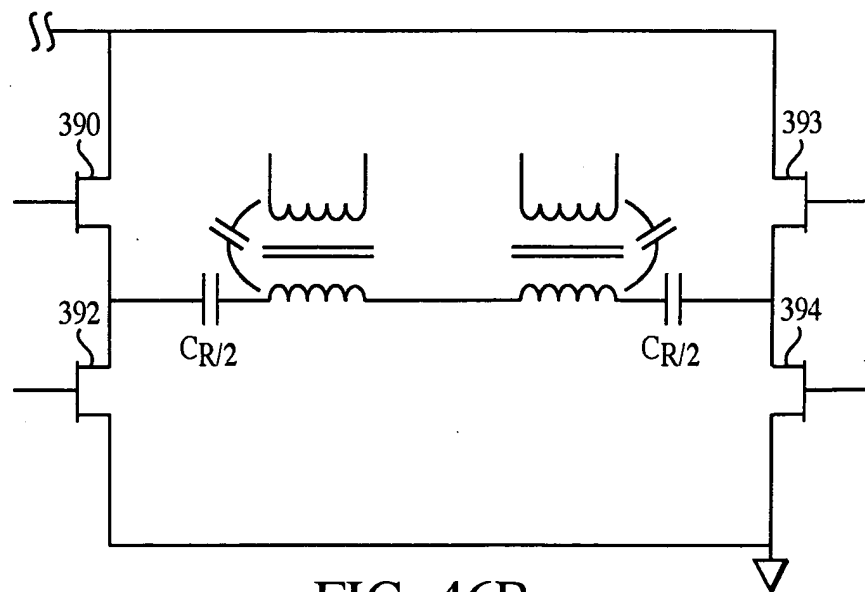


FIG. 46B

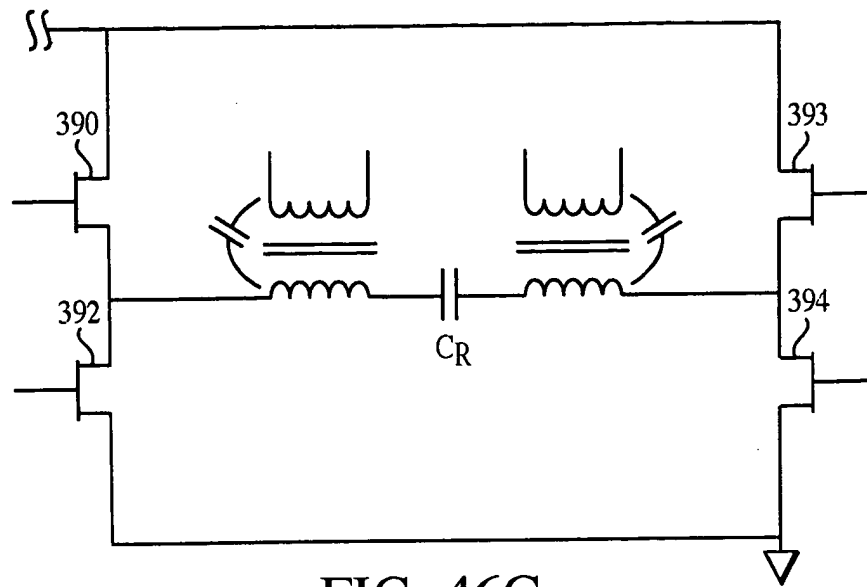
Applicant(s): Patrizio Vinciarelli  
OUTPUT RESISTANCE MODULATION IN POWER  
CONVERTERS

FIG. 46C

Applicant(s): Patrizio Vinciarelli  
OUTPUT RESISTANCE MODULATION IN POWER  
CONVERTERS

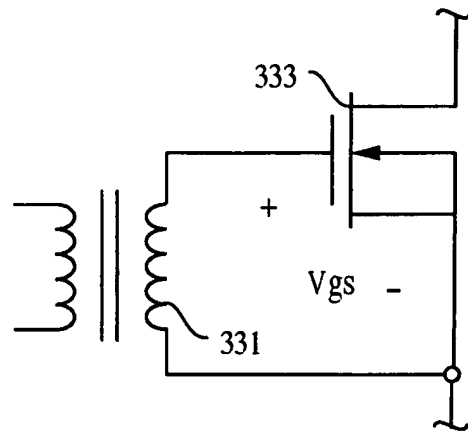


FIG. 47A

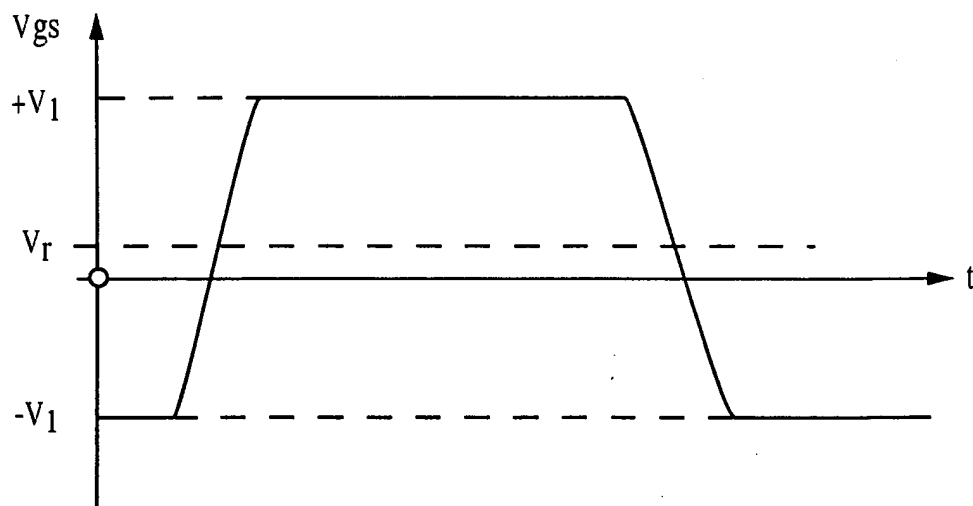


FIG. 47B

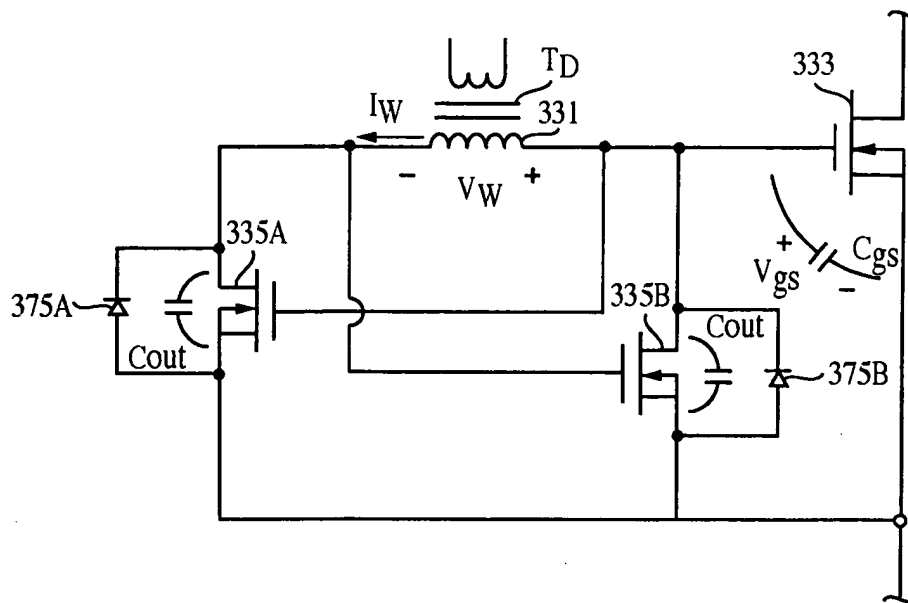
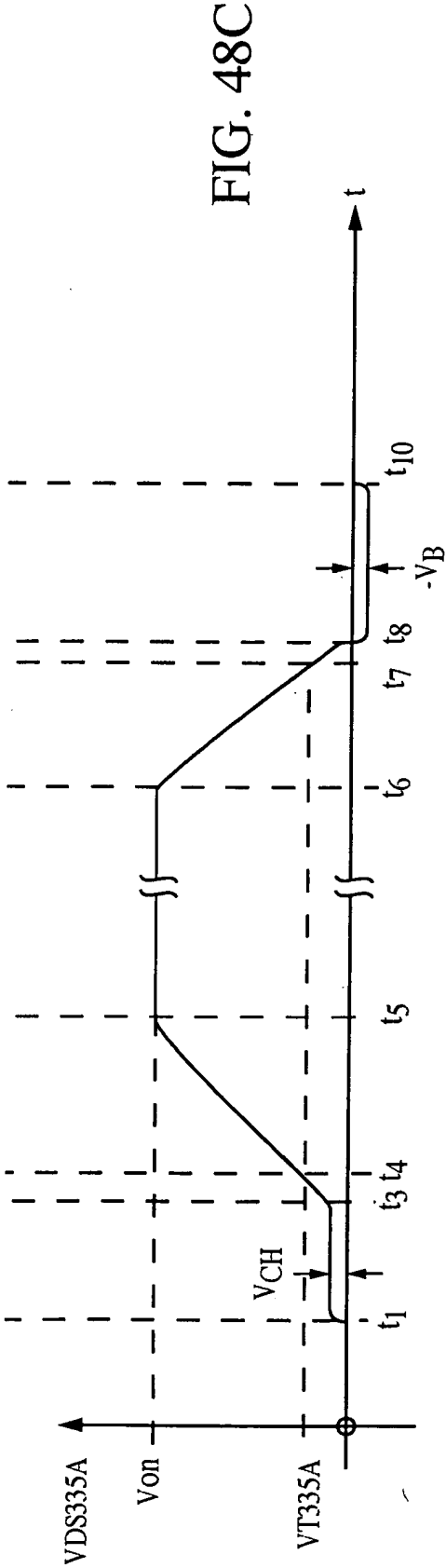
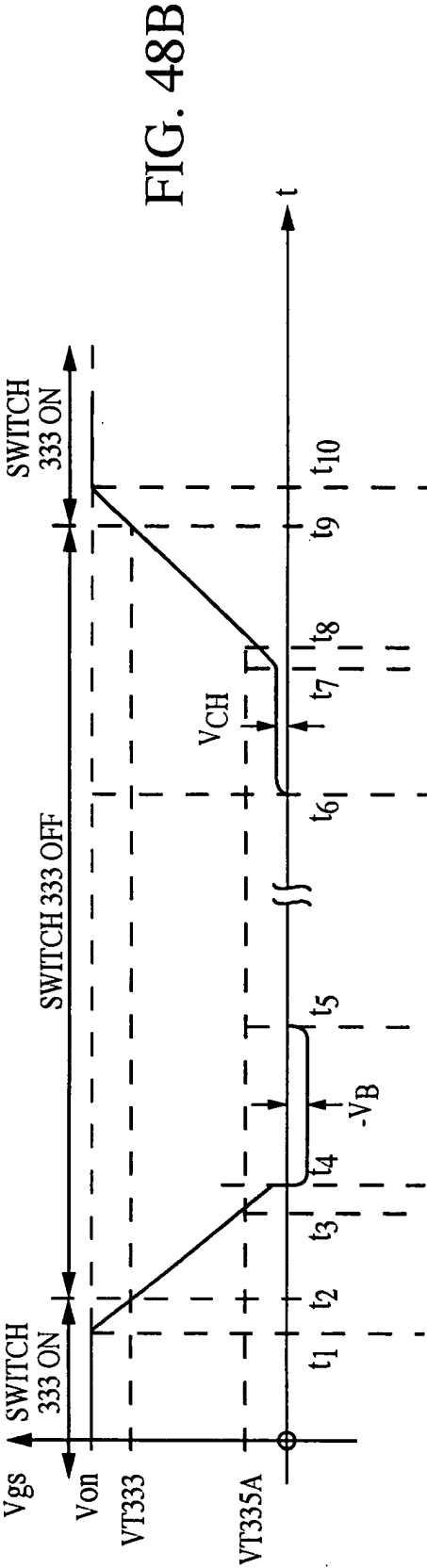


FIG. 48A





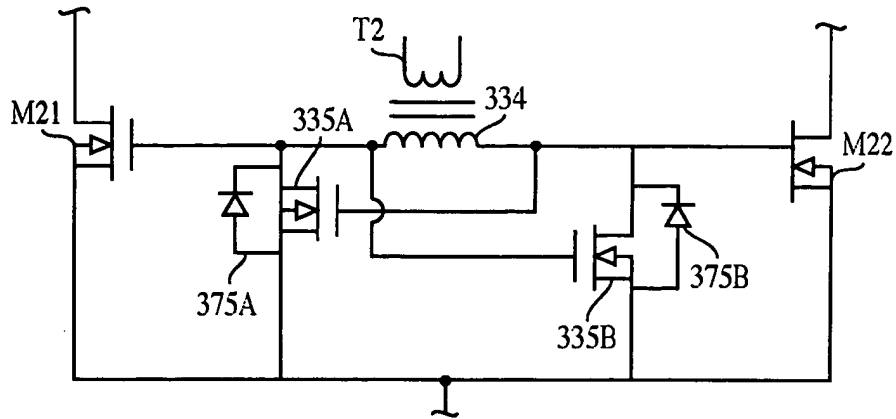


FIG. 49

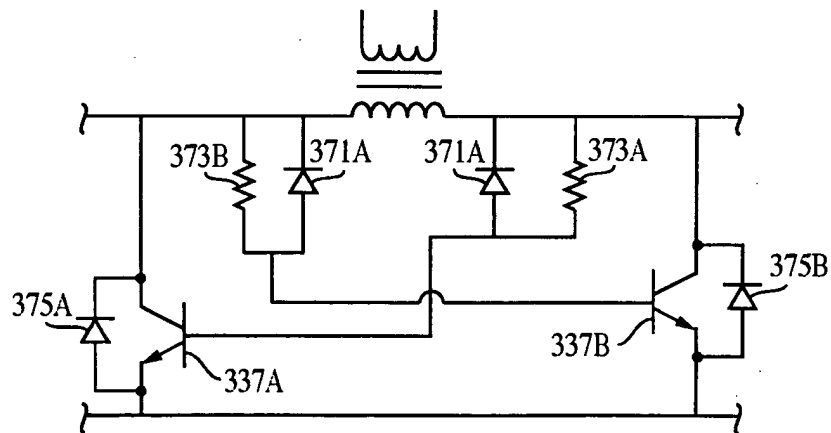


FIG. 50

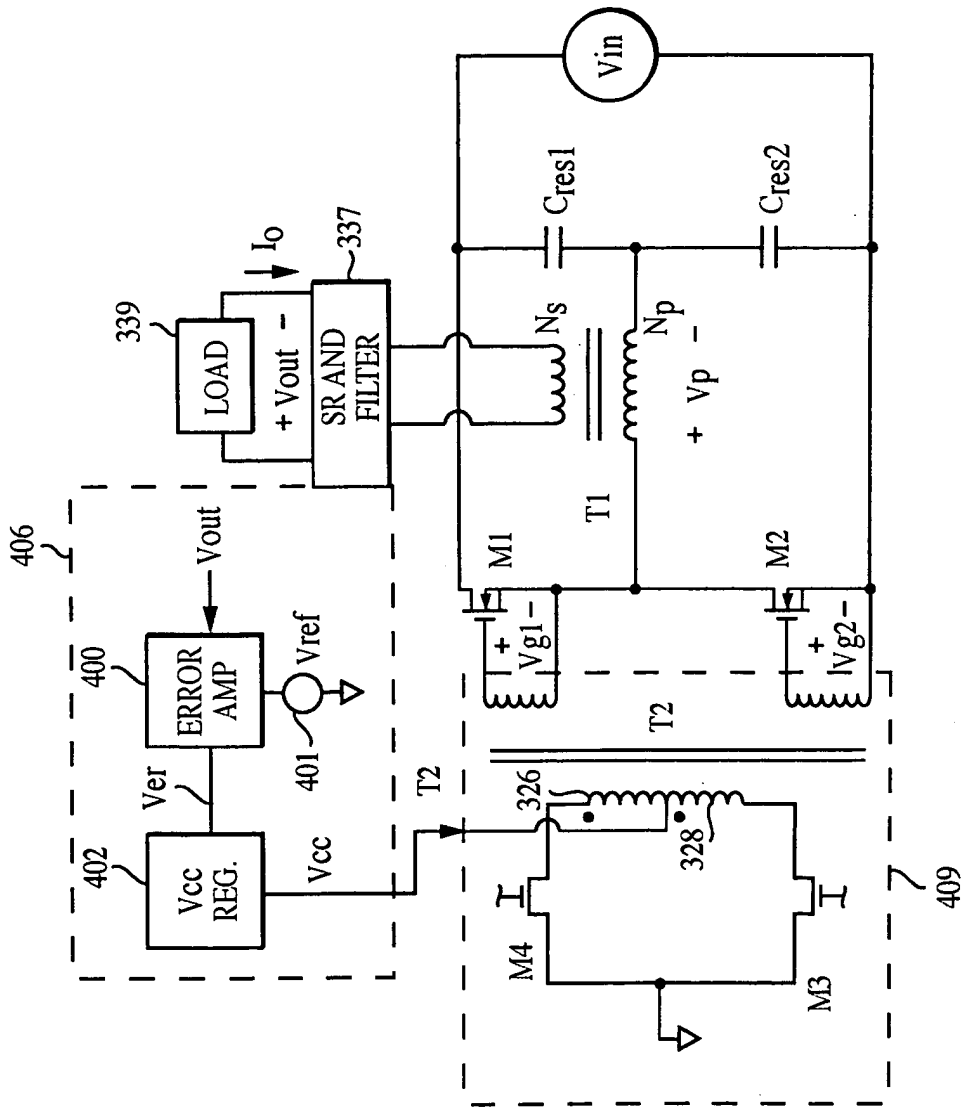


FIG. 51

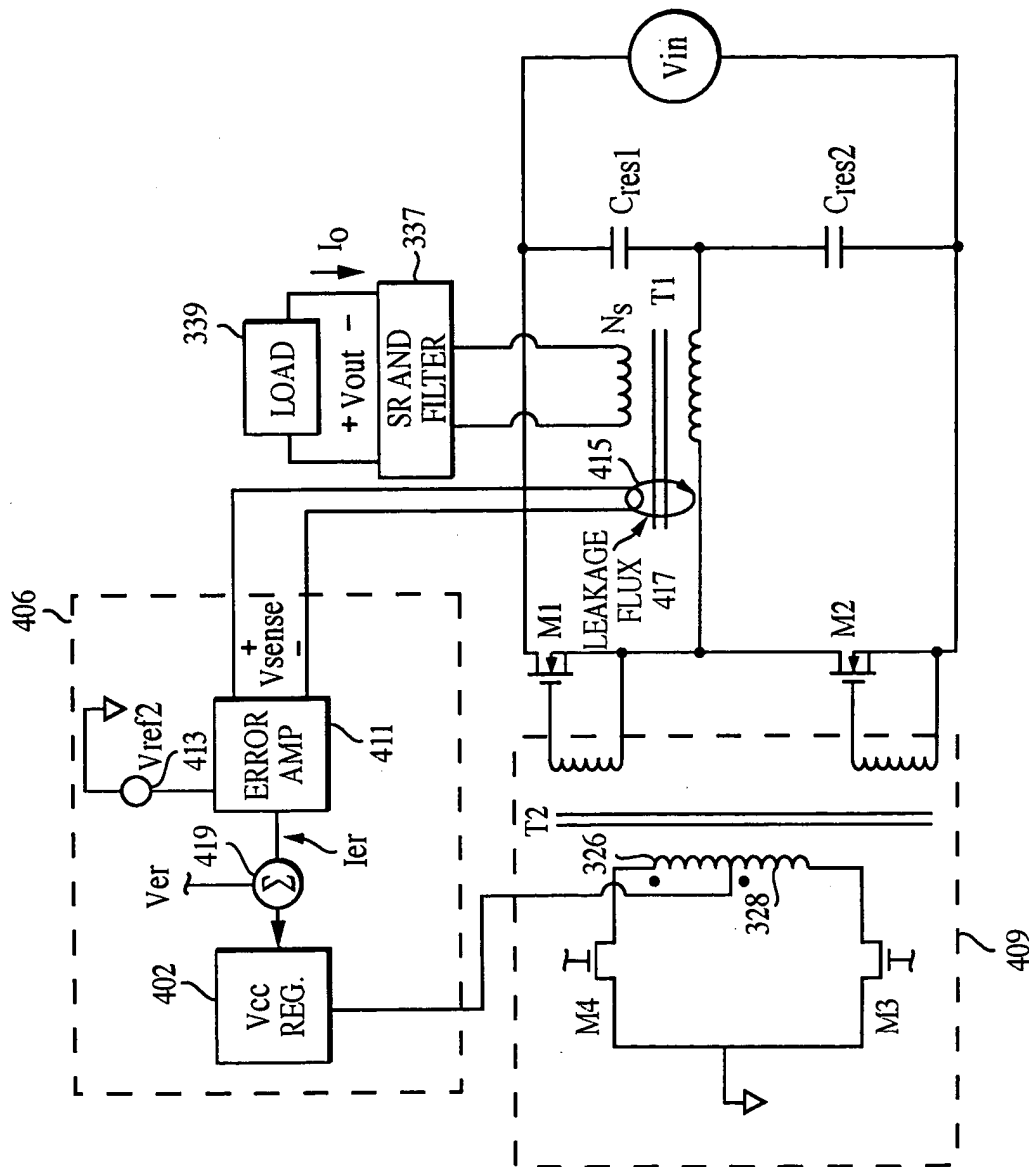


FIG. 52

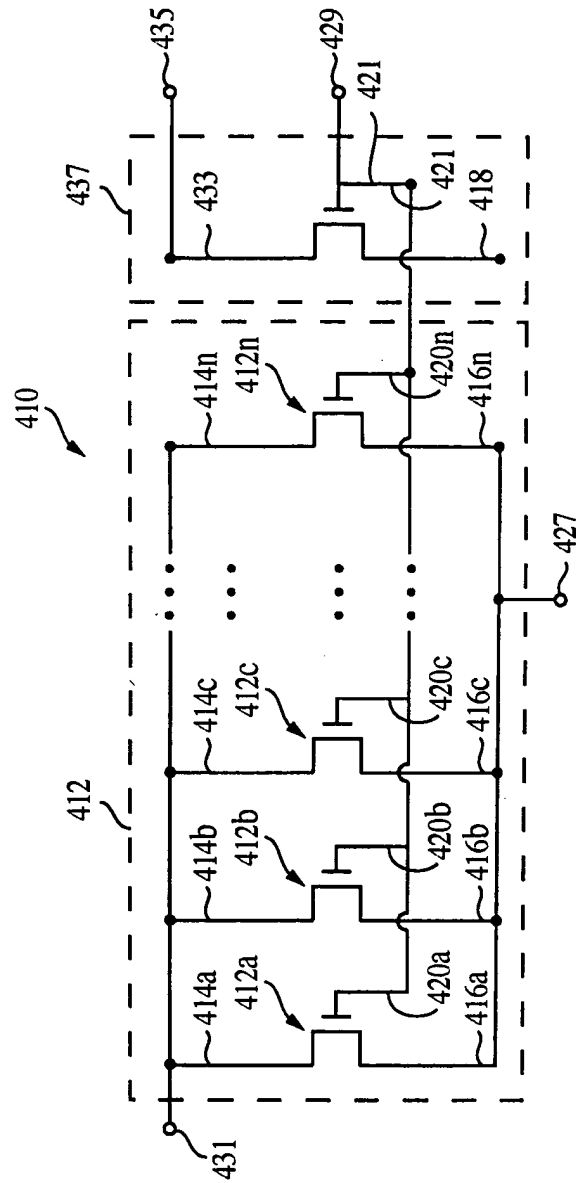


FIG. 53

Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER CONVERTERS

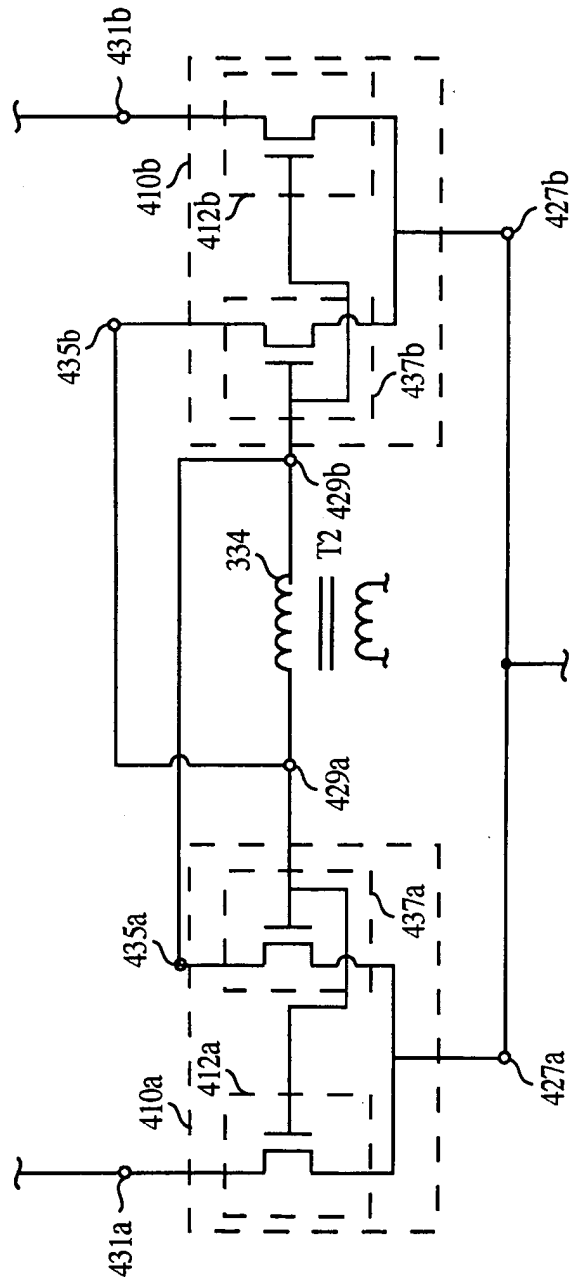


FIG. 54

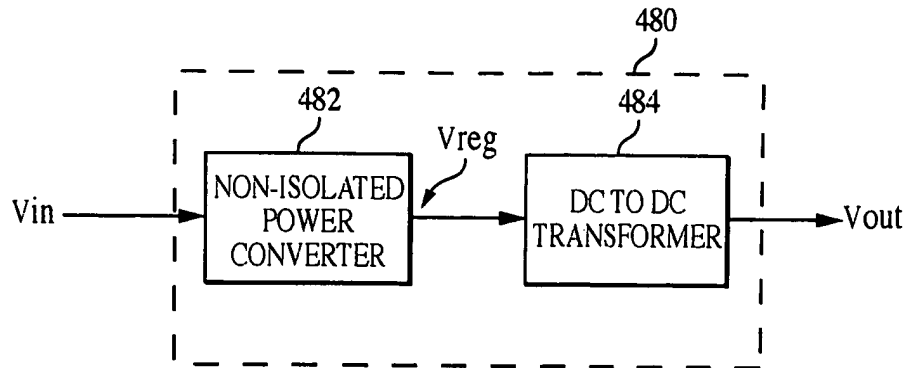


FIG. 55A

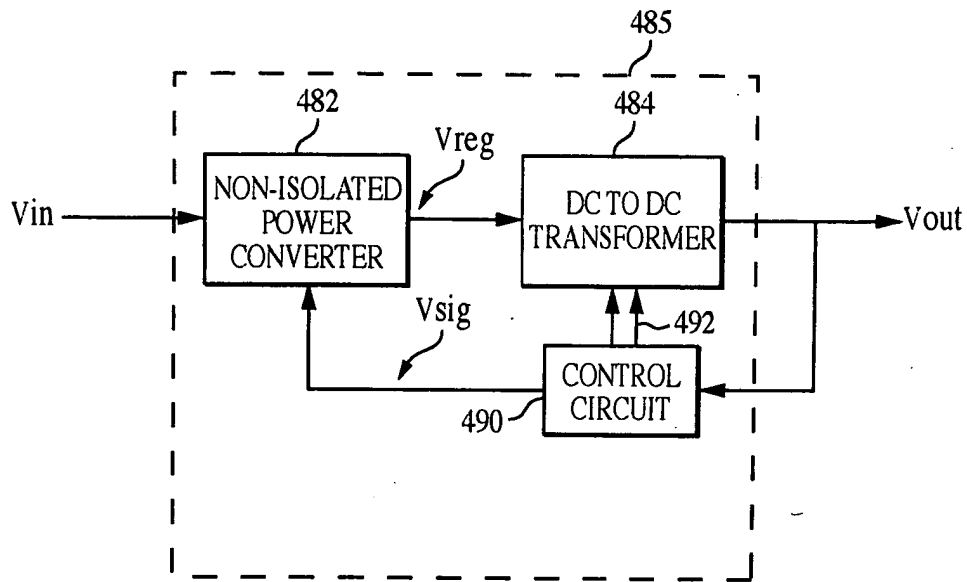


FIG. 55B

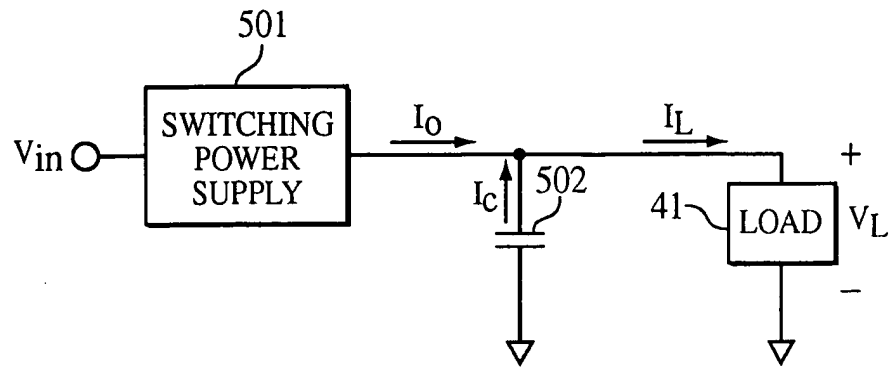


FIG. 56

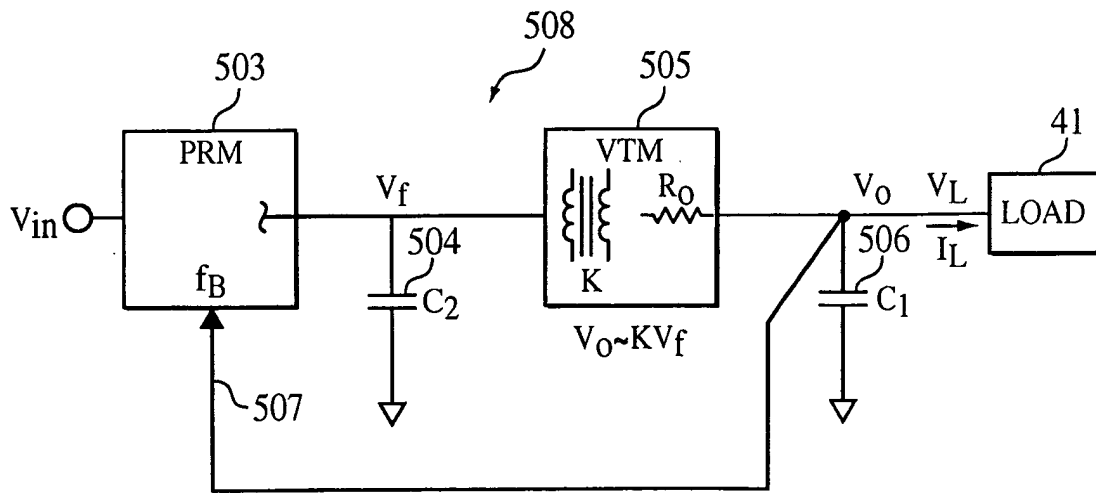


FIG. 58



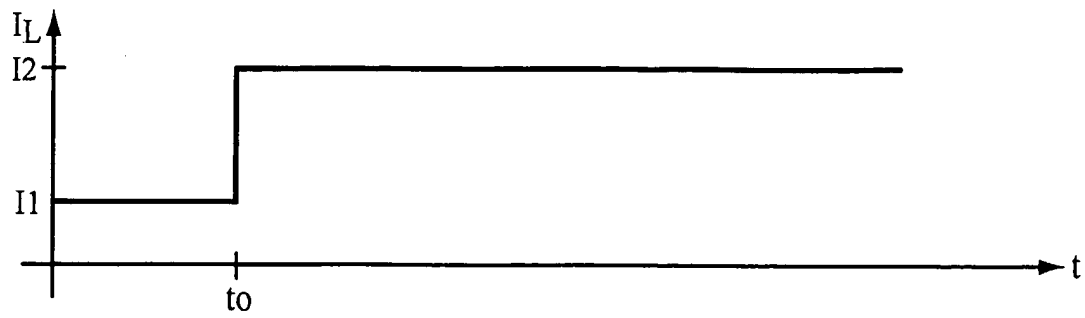


FIG. 57A

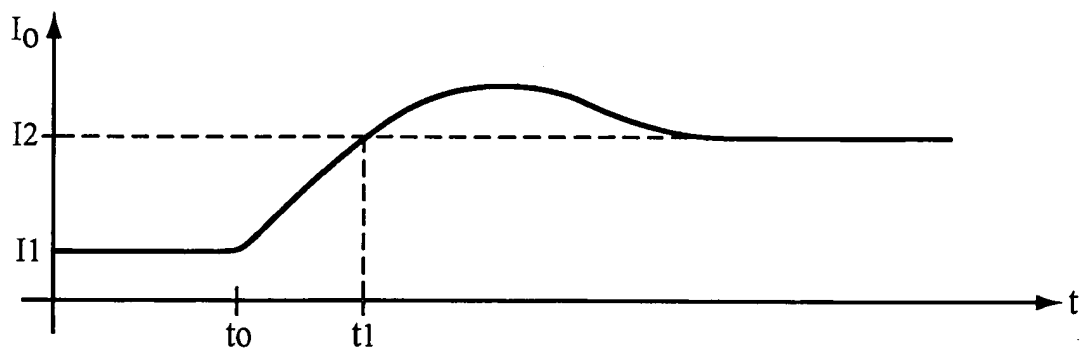


FIG. 57B

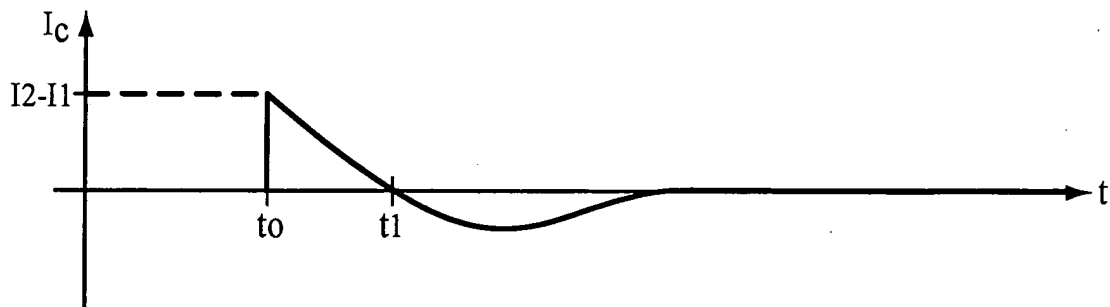


FIG. 57C

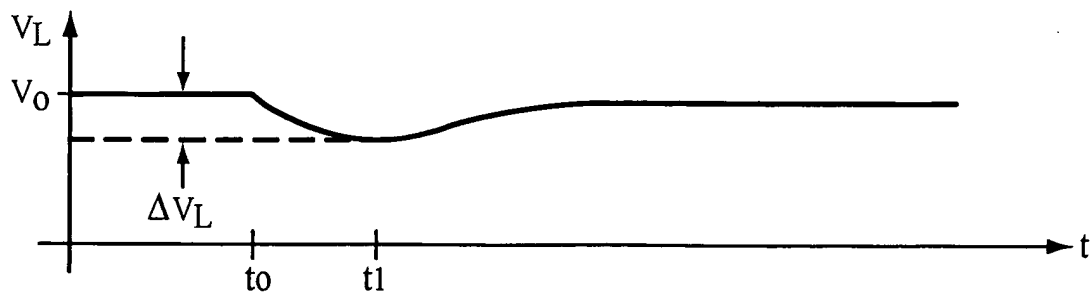


FIG. 57D

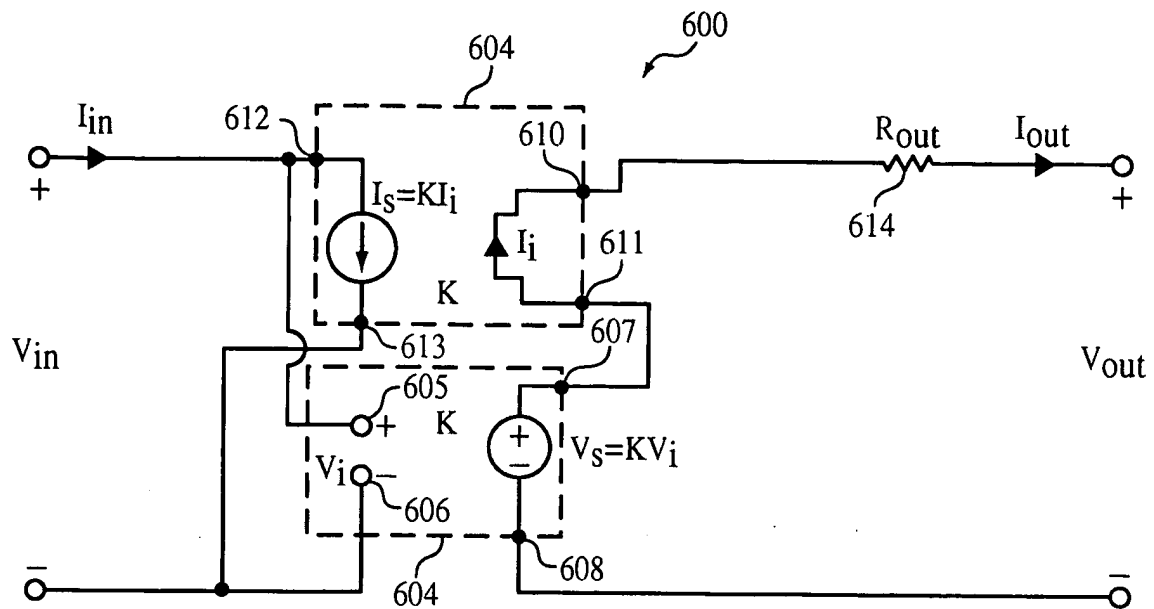


FIG. 59A

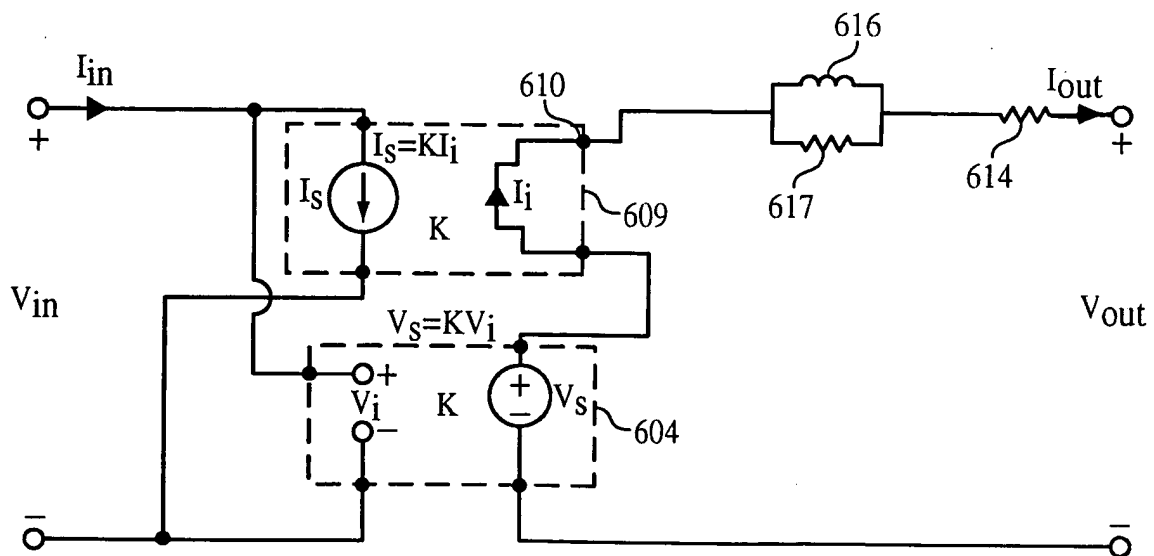


FIG. 59B

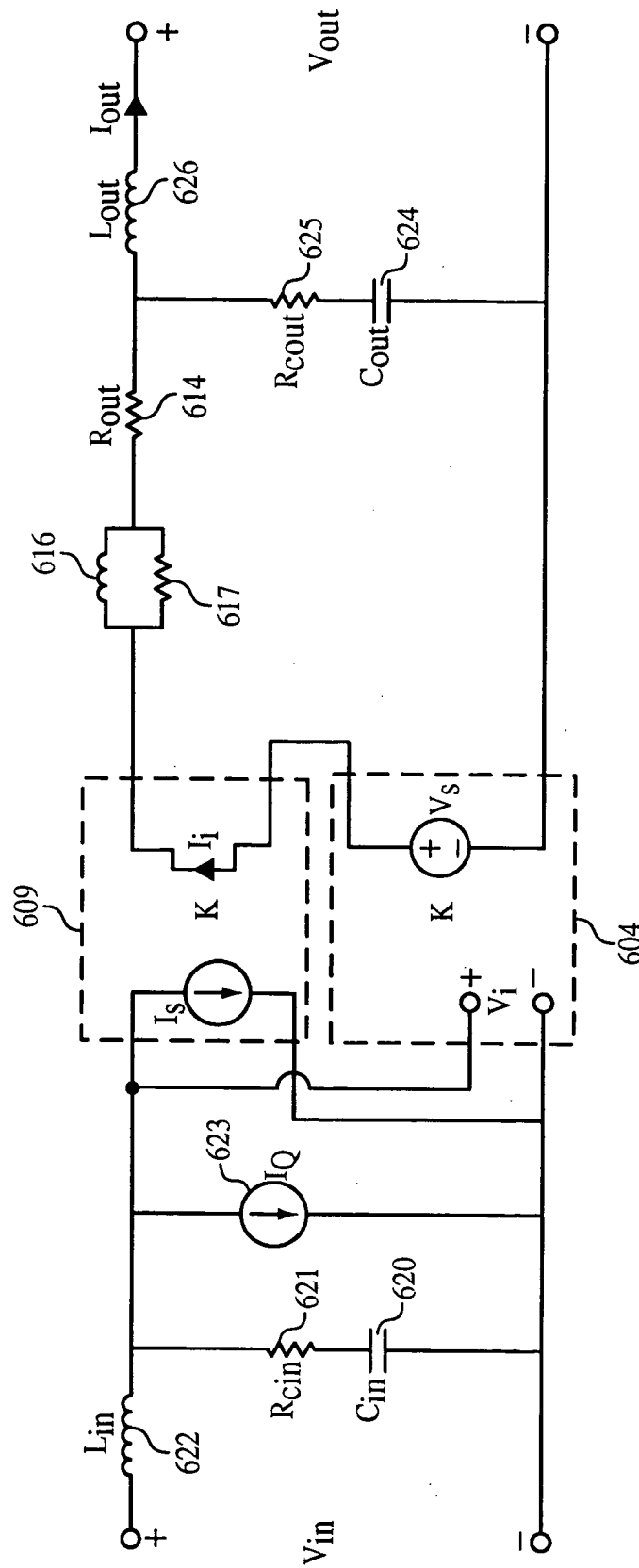


FIG. 59C